

Cm#: FAST-TEV-071

EMC TESTING ON THE FAST ETU IDPU

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INTRODUCTION

The EMC test on the FAST flight payload consisted of a full system test in which all components of the system were operated simultaneously and the output data from each instrument examined to verify that there was no significant raising of the noise floor in any system caused by the operation of any other system. This test was done with the full payload only operating with the payload GSE, and with the full payload operating in the spacecraft with other spacecraft systems including the transmitter and the torque coils operating. No interference was found in either test except for a small effect from the torque coils seen in the E-Field data, and a very large effect from the torque coils on the magnetometer. The latter was of course expected. Since it is not planned to operate the torque coils during science data taking, this is not considered to be a problem.

Since the above described testing was done, the Project has asked UCB to do perform Mil-461 type conducted susceptibility testing on the ETU IDPU to get some indication of the level of margin which we have against electromagnetic interference in the payload system. The tests to be performed are CS01, CS02, and CS06 per the methods of Mil-462, and to the levels specified in the SMEX Project document FAST-012. Testing is to be done on the input power line only of the ETU IDPU assembly. This report gives the results of that testing.

CS06 TEST PROCEDURE AND RESULTS

Figure 1 shows the set-up used to perform the CS06 test. The ETU IDPU was connected to the GSE system which simulates the spacecraft interface and allowed selected output data to be displayed. The ETU magnetometers were connected to the system, but no ESA's, boom units, or TEAMS were connected. The dummy load module provides a simulated load of 4 Radial Boom Units, and 2 ESA units. The CS06 Test Pulser was put in series with the power line so that it put pulses directly on the +28 volt power input. The GSE scripts TVSFA, TVADC1, and TVSVY0 were run to look for interference in the E-Field system. SHOMON2 was also run to view the full engineering housekeeping, and to verify correct operation of the processor system.

Figure 2 below shows the voltage and current waveforms of the spikes put onto the input power line.

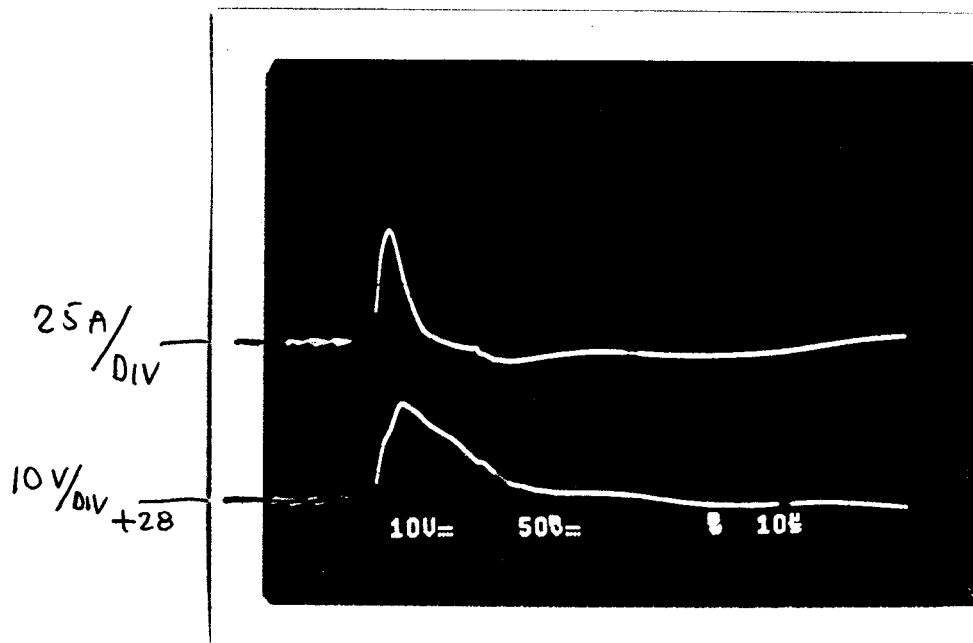


FIGURE 2

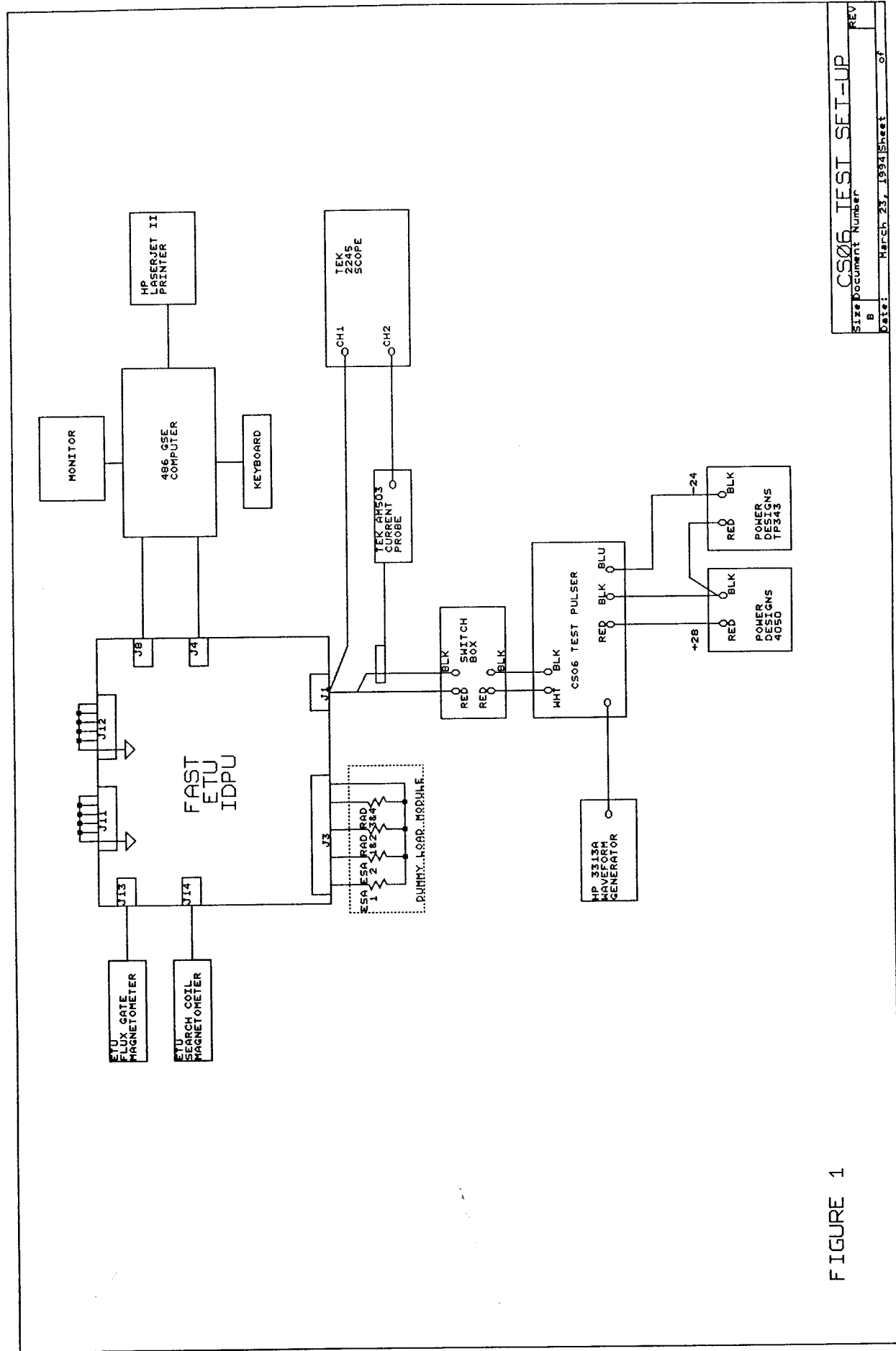


FIGURE 1

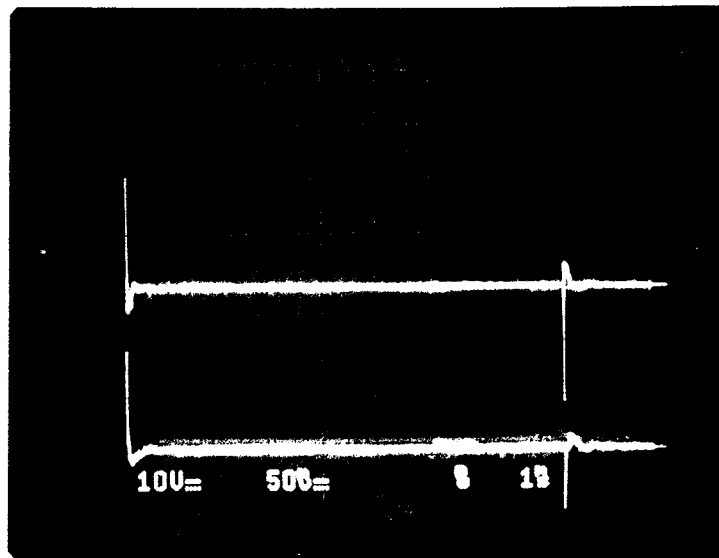


FIGURE 3

Figure 3 shows the same waveforms at a slower time scale so that it can be verified that the pulses are occurring with one positive pulse and one negative pulse 60 times each second. Note that the peak voltage of the pulses is ± 18 volts from the nominal +28 volt power line, and that a little over 50 Amperers is required to achieve this. Since 50 Amperers is the maximum capability of the pulser we used, we were not able to develop the full 28 volts across the input EMI filters in the IDPU power supply that is called for by Mil-462. We deemed this to be acceptable since there are no known interference sources on the spacecraft which can develop current pulses which approach 50 Amperers.

Figure 4 below shows the voltage on the internal +5 volt supply in the IDPU while the test pulses are present. The interference pulses are about 150 mV in amplitude. This will pose no problem for digital circuitry, but may cause some problems in the analog circuits as will be seen below.

CH1
+5V SUPPLY

CH1-CH2

CH2
LOCAL GND

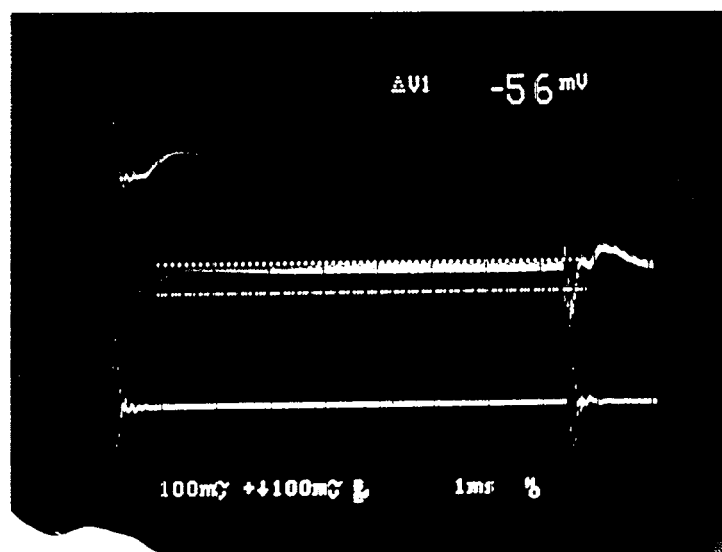


FIGURE 4

CH1
LOCAL GND

CH2 - CH1

CH2
+28 VOLTS
OUTPUT

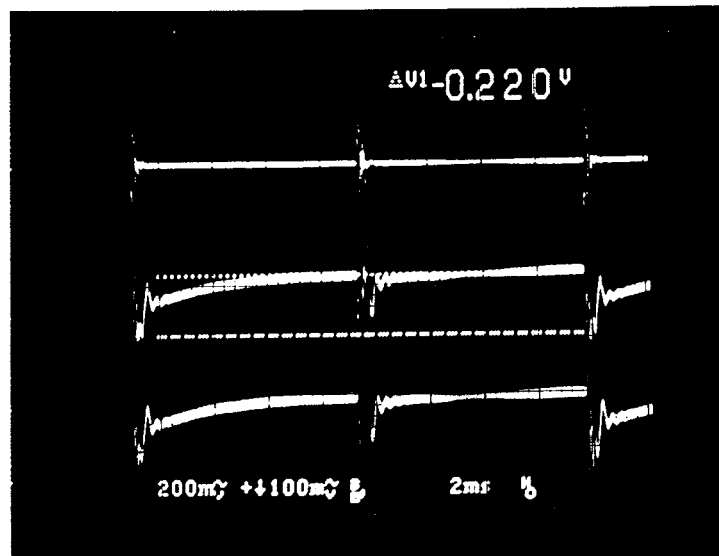


FIGURE 5

Figure 5 shows the interference which appears on the +28 volt output power from the IDPU to the ESA's and boom units. This voltage, of about 250 mV, is not expected to cause any problem with either the ESA's or the boom units.

Figure 6 is a print out from the GSE while running the TVADC1 script. Note the spurious data point (circled).

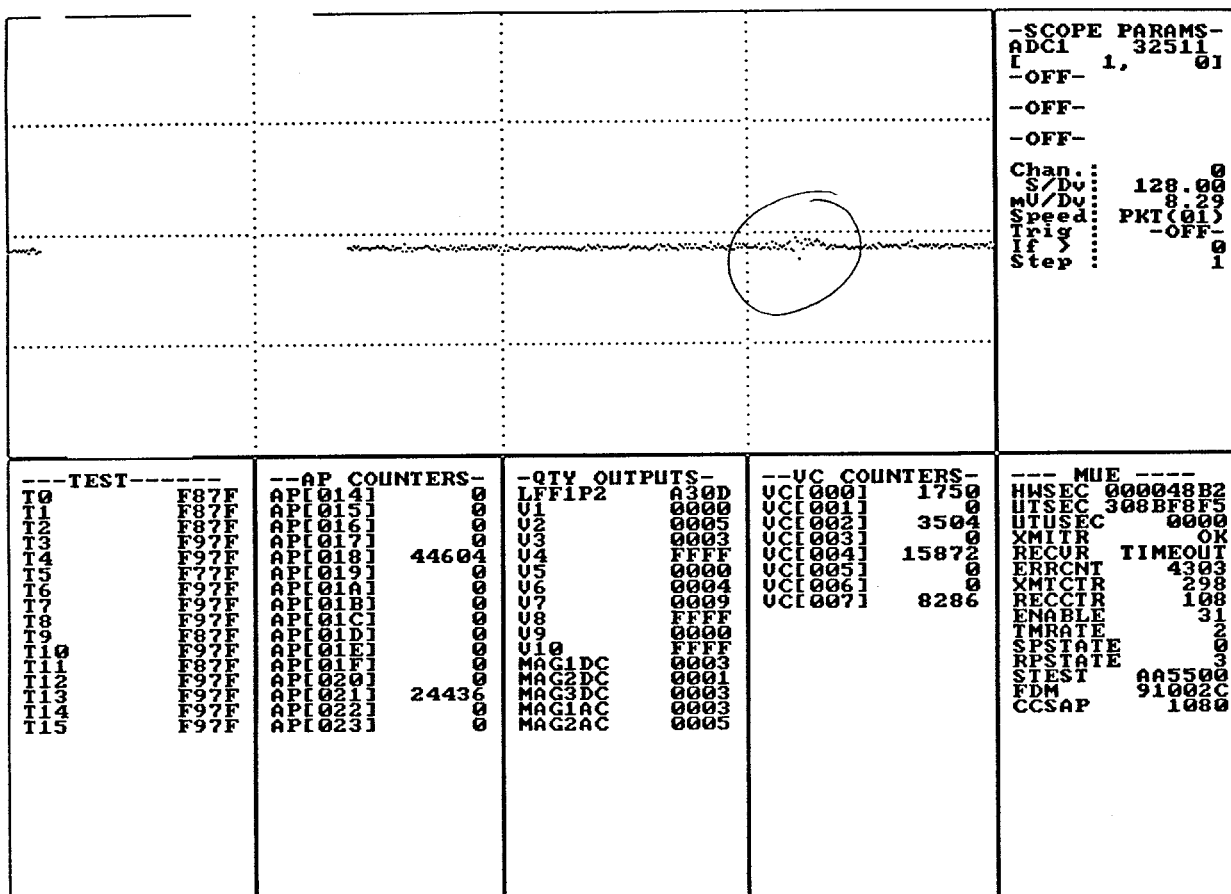
Figure 7 shows the output from TVSVY0. Note the same level of interference in the 3 ADC channels displayed. That the spurious data points appear to occur more often here is due to the lower sample rate of these channels.

Figure 8 shows the output from TVSFA. The six lines which stand above the noise floor are spurious. On repeated sweeps, this pattern shifts in phase showing that the interference is associated with the time that the pulses occur rather than being due to specific frequency components of the pulses.

-V₁ - V₄

16 k bits

sec

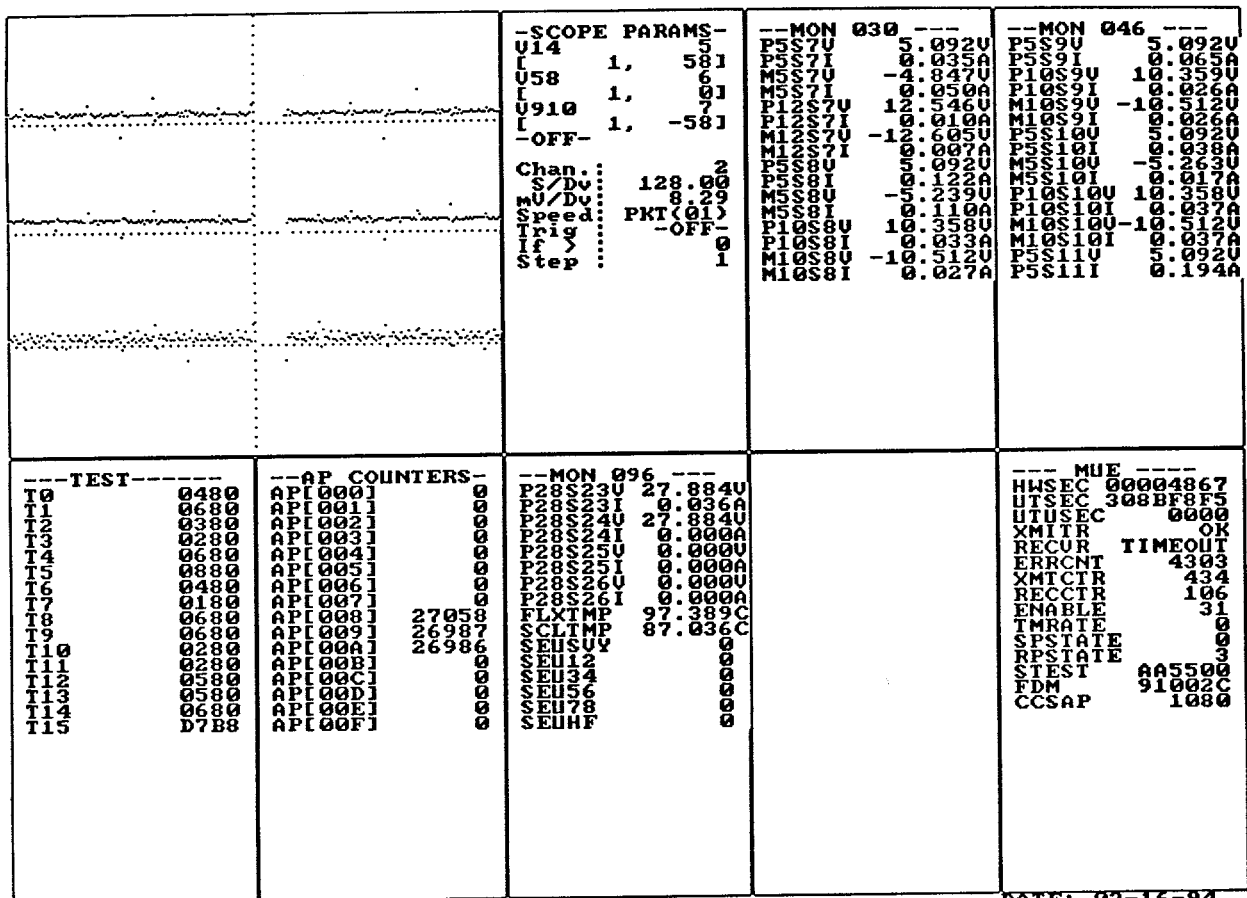


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UPDATE: 352 DATE: 03-16-94 GMT: 00:30:11

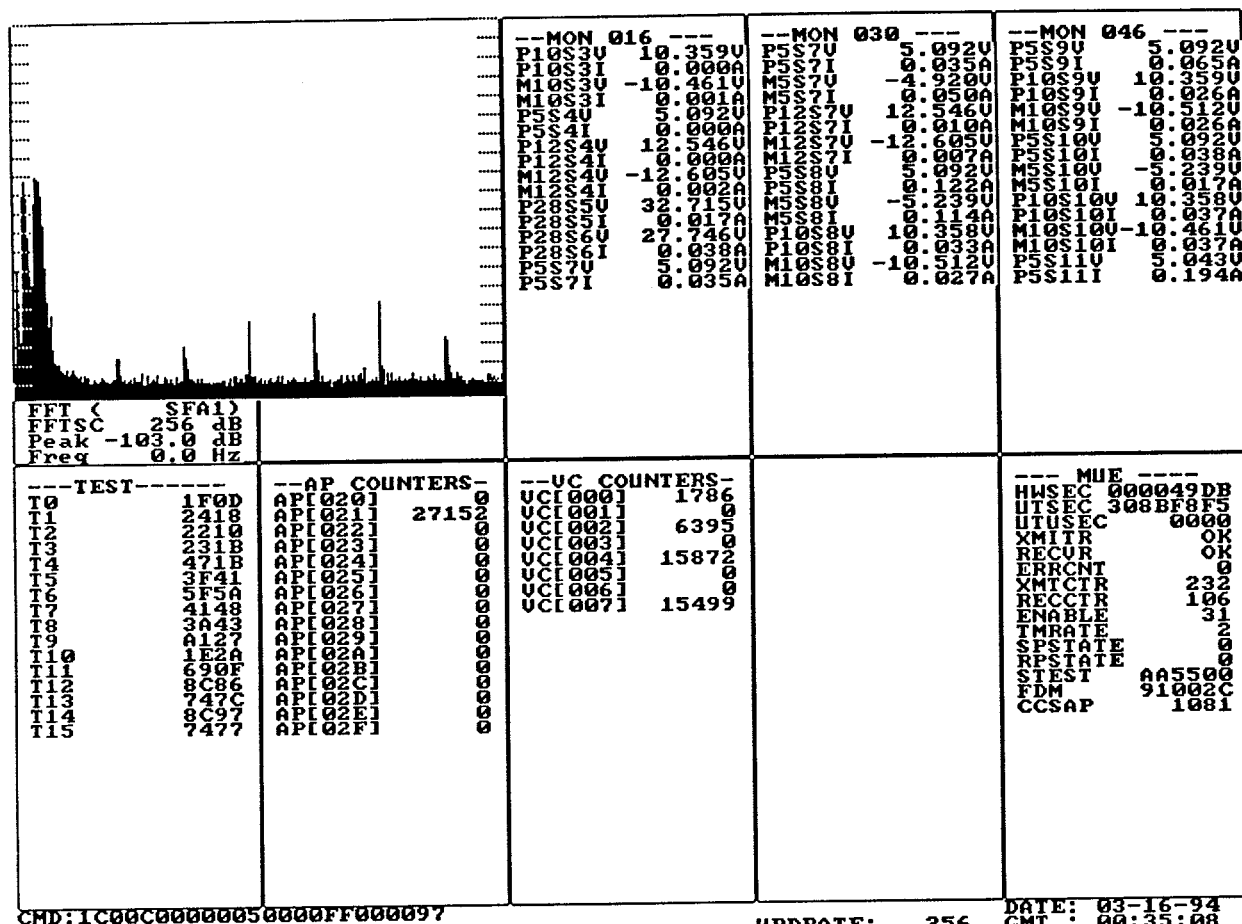
Figure 6

V₄
V₅ - V₈
V₉ - V₁₀
1 kbit/sec



16 MARCH, 1997
CS06 w/ 20V pulses

Figure 7



CS06 STIMULATION ON

Figure 8

CS01 TEST PROCEDURE AND RESULTS

Figure 9 shows the set-up used to perform the CS01 test. The ETU IDPU was connected to 486 GSE system which simulated the spacecraft interface and allowed selected output data to be displayed. The ETU magnetometers were connected to the system, but no ESA's, boom units, or TEAMS were connected. The dummy load module provided a simulated load of 4 Radial Boom Units and 2 ESA units. The current amplifier driven by a sine wave from the HP 3313A was put in series with the power line so that the waveform specified by CS01 was added to the DC value of the voltage on the +28 volt power input. For frequencies from 30 Hz to 2 kHz, the added signal was an unmodulated sine wave. From 2kHz to 50 kHz, the waveform was a sine wave which was modulated with a 50% duty cycle at 1000 Hz. From 30 Hz to 1500 Hz, the amplitude of the unmodulated sine wave was 2.8 volts rms. From 1500 Hz it was ramped down to 1 volt rms at 50 kHz (per figure 5.2.2.1-1 of FAST-SPEC-001). The input amplitude was also limited by the constraint that the AC component of the input current shall not exceed 6 Amperers peak to peak.

The GSE scripts TVSFA, TVADC1, and TVSVY0 were run to look for interference in the E-Field system. SHOMON2 was also run to view the full engineering housekeeping and to verify correct operation of the processor system, and the value of the voltage which fed thru to the internal +5 volt supply and to the +28 volts supplied by the IDPU to the external instruments.

Table 1 below summarizes the effects on the other supplies and the operation of the instrument and shows the value of the voltage which was attained on the input power line as a function of input frequency.

Table 1 -- Summary of CS01 Test Results

Input Freq	V p-p	AC Current on Input	AC on IDPU Int. 5 v bus	AC on IDPU Supplied +28	Disturbance on:			
					SFA	ADC	SVY	MON
15 Hz	9 Vp-p	100 mA p-p	50 mVp-p		X	X	X	N
30 Hz	9 Vp-p	300 mA p-p	110 mVp-p	1.3 Vp-p	VL	N	N	N
60 Hz	9 Vp-p	400 mA p-p	200 mVp-p	1.8 Vp-p	X	X	X	N
100 Hz	9 Vp-p	750 mA p-p	320 mVp-p	2.0 Vp-p	X	L	X	N
200 Hz	9 Vp-p	1.9 A p-p	580 mVp-p	2.0 Vp-p	VL	X	M	N
290 Hz	8 Vp-p	2.5 A p-p	750 mVp-p	2.2 Vp-p	M	M	S	N
500 Hz	8 Vp-p	3.7 A p-p	450 mVp-p	1.8 Vp-p	M	M	S	N
1 kHz	8 Vp-p	4.5 A p-p	210 mVp-p	1.7 Vp-p	M	M	S	N
2 kHz	5 Vp-p	6 A p-p	130 mVp-p	.85 Vp-p	L	M	M	N
2 kHz	5	6 A p-p	200 mVp-p	.85 Vp-p	X	X	X	N
4 kHz	2.5	6 A p-p	200 mVp-p	.42 Vp-p	L	L	M	N
8 kHz	2.5	6 A p-p	300 mVp-p	.29 Vp-p	X	N	L	N
13 kHz		6 A p-p		.58 Vp-p	X	X	X	N
16 kHz	1	6 A p-p	390 mVp-p	.54 Vp-p	L	X	X	N
32 kHz		6 A p-p		.48 Vp-p	X	X	X	N
50 kHz	3	6 A p-p	300 mVp-p	.62 Vp-p	VL	N	N	N
80 kHz		6 A p-p		.75 Vp-p	X	X	X	N
100 kHz	4.5	6 A p-p		.80 Vp-p	N	N	X	N

and: N -- No Effect
M -- Moderate Effect

VL -- Very Low
S -- Severe Effect

L -- Low Effect

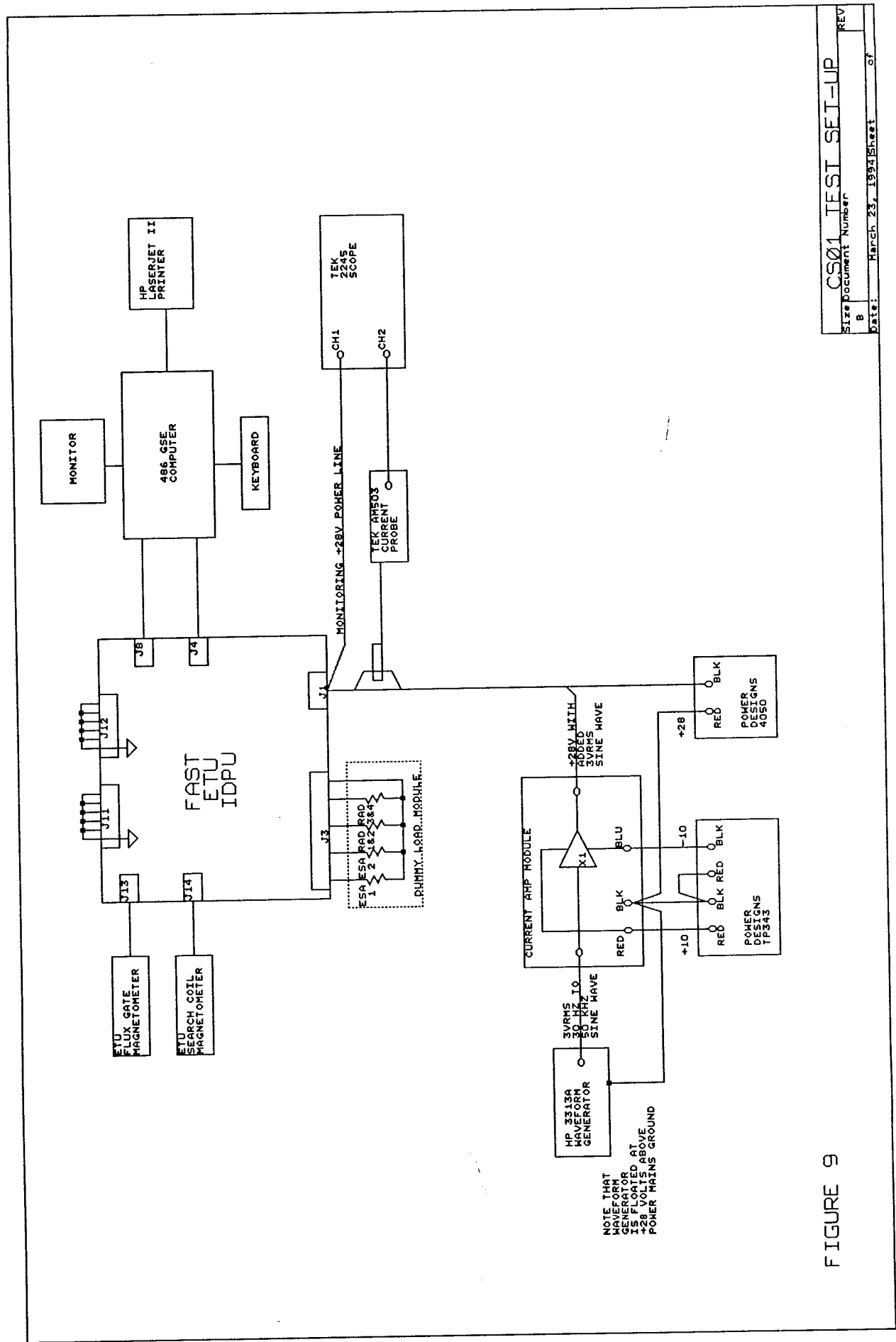


FIGURE 9

Figures 10 thru 39 show data output from the instrument under the various conditions of stimulation and provide the basis for the assessment of the operation shown in the four right columns of Table 1.

The E-Field instrument shows some sensitivity to this test. Disturbance of the science data starts at around 100 Hz and continues to about 10 kHz with a peak at 290 Hz. Examination of the IDPU internal +5 volt power bus and the +28 volt bus which is supplied to the external instruments shows that the effect on those lines also peaks at 290 Hz. From that we conclude that the primary way in which the conducted interference affects the science data is via less than ideal power line rejection in the analog circuits in the E-Field electronics.

				-SCOPE PARAMS- ADC1 32511 [-OFF- 1, 0] -OFF- -OFF- Chan.: 0 S/Dv: 128.00 MU/Dv: 8.29 Speed: PKT(01) Trig: -OFF- If: 0 Step: 1
---TEST--- T0 F87F T1 F87F T2 F87F T3 F97F T4 F77F T5 F97F T6 F87F T7 F87F T8 FA7F T9 F87F T10 F97F T11 F87F T12 F87F T13 F87F T14 FA7F T15 F87F	---AP COUNTERS--- AP[014] 0 AP[015] 0 AP[016] 0 AP[017] 0 AP[018] 20189 AP[019] 0 AP[01A] 0 AP[01B] 0 AP[01C] 0 AP[01D] 0 AP[01E] 0 AP[01F] 0 AP[020] 0 AP[021] 15251 AP[022] 0 AP[023] 0	---QTY OUTPUTS--- LFF1P2 A50D U1 0000 U2 0007 U3 0003 U4 FFFF U5 0000 U6 0005 U7 0009 U8 FFFF U9 0000 U10 FFFF MAC1DC 0002 MAC2DCC 0001 MAC3DCC 0003 MAC1ACC 0004 MAC2AC 0006	---VC COUNTERS--- VCI0001 152 VCI0011 0 VCI0021 35441 VCI0031 0 VCI0041 37030 VCI0051 0 VCI0061 10128 VCI0071 0	---MUE--- HWSEC 00000663 UTSEC 308AEF06 UTUSEC 0000 XMITR OK RECUR CODE_ERR ERRCNT 358 XMITCTR 298 RECCTR 108 ENABLE 31 TMRATE 2 SPSTATE 0 RPSTATE 3 STEST AA5500 FDM 91002C CCSAP 1081

CMD: 1C00C000000500000300D9C0

UPDATE: 352 DATE: 03-15-94
GMT: 00:52:40

3017

Figure 11

		-SCOPE PARAMS- U14 1, 581 U58 1, 581 U910 1, -581 -OFF- Chan.: 2 S/Dv.: 128.000 MU/Dv.: 8.299 Speed: PKT(01) Trig: -OFF- If: 0 Step: 1	--MON 030 -- P5570 5.116V P5571 -0.035A M5570 -4.920V M5571 -0.050A P12570 12.781V P12571 -0.009A M12570 -1.346V M12571 -0.007A P5580 0.114V P5581 -0.000A M5580 -0.214V M5581 -0.000A P10580 10.204V P10581 -0.033A M10580 -1.025V M10581 0.026A	--MON 046 -- P5590 5.190V P5591 -0.044A M5590 10.153V M5591 -0.026A P10590 -1.051V P10591 -0.027A M10590 -0.141V M10591 -0.000A P5510 0.198V P5511 -0.000A M5510 -0.000A M5511 -0.000A P10510 10.000V P10511 -1.000V M10510 -0.000A M10511 -0.000A
---TEST--- T0 0880 T1 0780 T2 0480 T3 0380 T4 0780 T5 0580 T6 0180 T7 0680 T8 0780 T9 0380 T10 0380 T11 0680 T12 0680 T13 0680 T14 0680 T15 0680	---AP COUNTERS--- API000 0 API001 0 API002 0 API003 0 API004 0 API005 0 API006 0 API007 0 API008 1132 API009 1130 API010 1130 API011 0 API012 0 API013 0 API014 0 API015 0	--MON 096 -- P28323 0.000V P28324 0.000A M28323 7.178V M28324 0.000A P28325 0.000V P28326 0.000A M28325 0.000A M28326 0.000A FLXIMP 149.000C SCLTMP 149.000C SEUSV 0 SEU12 0 SEU34 0 SEU56 0 SEU78 0 SEUHF 0	--- MUE --- HWSEC 000005C7 UIUSEC 308AEF66 UTUSEC 000000 XMITR 000000 RECUR CODE 000000 ERRCNT 000000 XMTCTR 000000 RECCTR 108 ENABLE 31 TMRATE 0 RPSTATE 0 SPSTATE 0 STEST 0A5500 FDM 91002C CCSAP 1085	

CHD:1C00C000001D000000000A8CD

UPDATE: 160

DATE: 03-15-94
GMT: 00:50:05

30 Hz

Figure 12

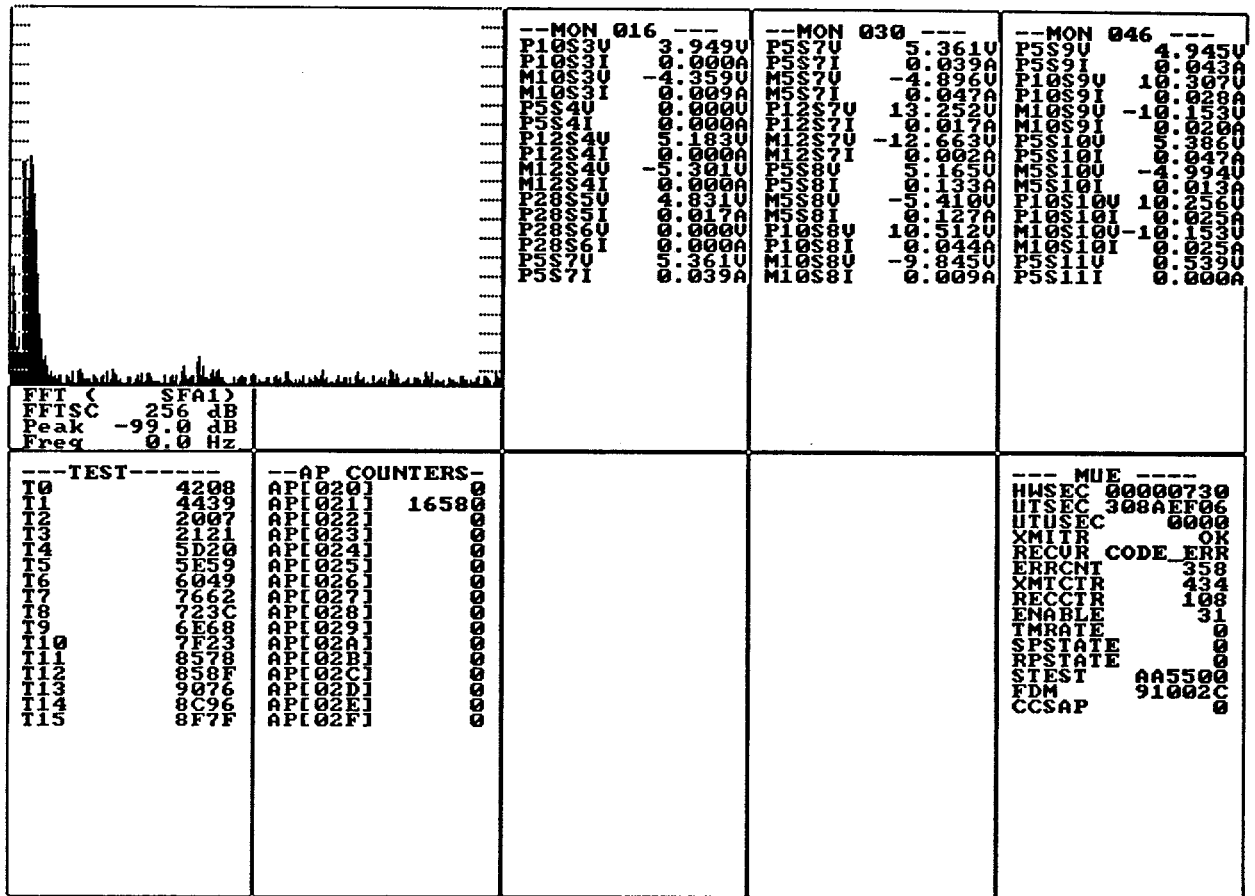
				-SCOPE PARAMS- ADCI -7 L-OFF- 1, 01 -OFF- -OFF- Chan.: 0 S/Dv: 128.00 U/Dv: 8.29 Speed: PKT(01) Trig: -OFF- Step: 0 1
---TEST--- T10 F97F T11 F97F T12 F97F T13 F97F T14 F97F T15 F97F T16 F97F T17 F97F T18 F97F T19 F97F T110 F97F T111 F97F T112 F97F T113 F97F T114 F97F T115 F87F	--AP COUNTERS-- AP[014] 0 AP[015] 0 AP[016] 0 AP[017] 0 AP[018] 21053 AP[019] 0 AP[01A] 0 AP[01B] 0 AP[01C] 0 AP[01D] 0 AP[01E] 0 AP[01F] 0 AP[020] 15251 AP[021] 0 AP[022] 0 AP[023] 0	-QTY OUTPUTS- LFF1P2 A50D U1 0000 U2 0007 U3 0003 U4 FFFF U5 0000 U6 0005 U7 0009 U8 FFFF U9 0000 U10 FFFF MAG1DC 00002 MAG2DC 00001 MAG3DC 00003 MAG1AC 00004 MAG2AC 00006	--UC COUNTERS-- UCC[000] 153 UCC[001] 36305 UCC[002] 37030 UCC[003] 0 UCC[004] 0 UCC[005] 18014 UCC[007] 0	---MUE--- HWSEC 0000068C UTSEC 308AF06 UTUSEC 0000 XMTR OK RECUR CODE ERR ERRCNT 358 XMTCR 232 RECCTR 106 ENABLE 31 TMRATE 2 SPSTATE 1 RPSTATE 3 STEST AA5500 FDM 91002C CCSAP 1080

CMD:IC00C000000500000300D9C0

UPDATE: 400 DATE: 03-15-94
GMT: 00:53:22

100 Hz

Figure 13

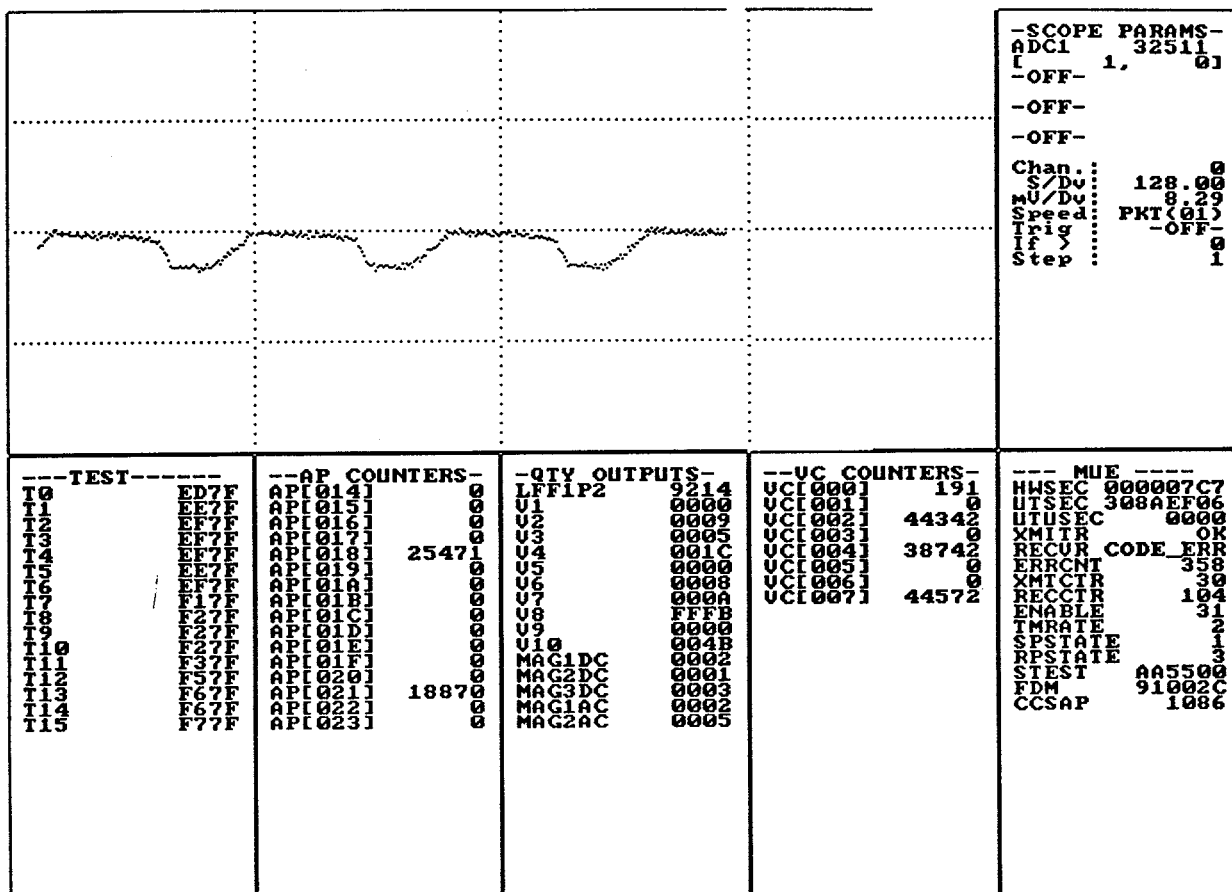


CMD:1C00C000000500000005A8CD

UPDATE: 208 DATE: 03-15-94
 GMT: 00:56:06

200 Hz

Figure 14



CHD:1C00C000000500000300D9C0

UPDRATE: 352

DATE: 03-15-94
 GMT: 00:58:36

290 Hz

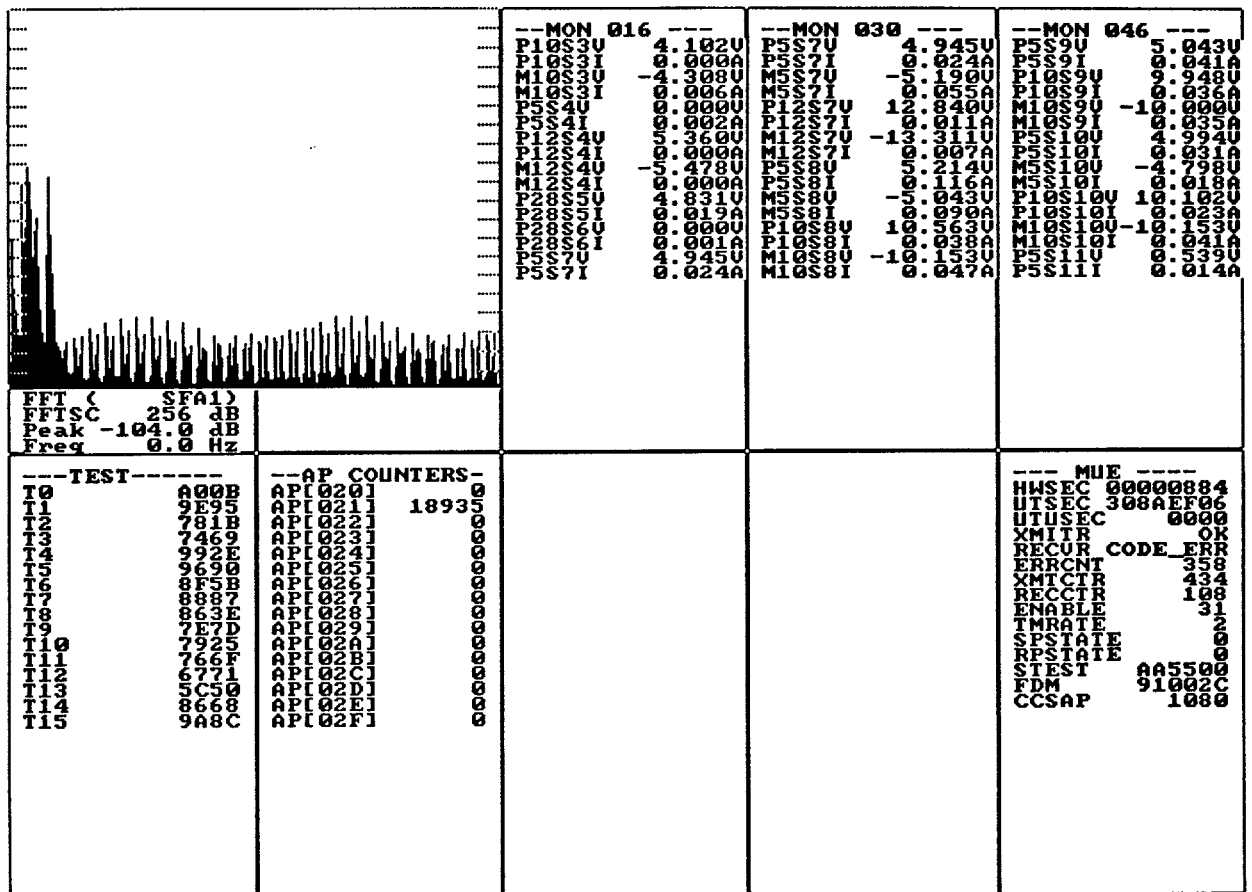
Figure 17

		<pre>--SCOPE PARAMS-- U14 1, 581 U58 1, 581 U910 1, -581 --OFF-- Chan.: 2 S/Dv: 128.000 M/Dv: 8.29 Speed: PKT(01) Trig: -OFF- If: 0 Step: 1</pre>	<pre>--MON 030 -- P5000U 0.435V P5000I 0.000A M5000U 0.000A M5000I 0.000A P10000U 0.000V P10000I 0.000A M10000U 0.000A M10000I 0.000A P50000U 0.000V P50000I 0.000A M50000U 0.000A M50000I 0.000A P100000U 0.000V P100000I 0.000A M100000U 0.000A M100000I 0.000A</pre>	<pre>--MON 046 -- P5000U 0.435V P5000I 0.000A M5000U 0.000A M5000I 0.000A P10000U 0.000V P10000I 0.000A M10000U 0.000A M10000I 0.000A P50000U 0.000V P50000I 0.000A M50000U 0.000A M50000I 0.000A P100000U 0.000V P100000I 0.000A M100000U 0.000A M100000I 0.000A</pre>
<pre>--TEST-- T0 0180 T1 0580 T2 FF7F T3 0280 T4 0A80 T5 0780 T6 0A80 T7 0180 T8 0980 T9 0880 T10 FF7F T11 0380 T12 0B80 T13 0880 T14 EB7F T15 4EBE</pre>	<pre>--AP COUNTERS-- AP[000] 0 AP[001] 0 AP[002] 0 AP[003] 0 AP[004] 0 AP[005] 0 AP[006] 0 AP[007] 0 AP[008] 13067 AP[009] 13053 AP[00A] 13048 AP[00B] 0 AP[00C] 0 AP[00D] 0 AP[00E] 0 AP[00F] 0</pre>	<pre>--MON 096 -- P20000U 0.000V P20000I 0.000A M20000U 7.316V M20000I 0.000A P100000U 0.000V P100000I 0.000A M100000U 0.000V M100000I 0.000A P500000U 149.000V P500000I 149.000A M500000U 0.000V M500000I 0.000A P1000000U 0.000V P1000000I 0.000A M1000000U 0.000V M1000000I 0.000A</pre>	<pre>--- MUE --- HWSEC 000007F9 UTSEC 308AEF06 UTUSEC 0000 XMITR OK RECUR CODE_ERR ERRCNT 358 XMITCTR 232 RECCTR 106 ENABLE 31 TMRATE 0 SPSTATE 0 RPSTATE 0 STEST 0A55000 FDM 910000 CCSAP 1000</pre>	

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UPDATE: 144 DATE: 03-15-94
GMT: 00:59:11

290 Hz



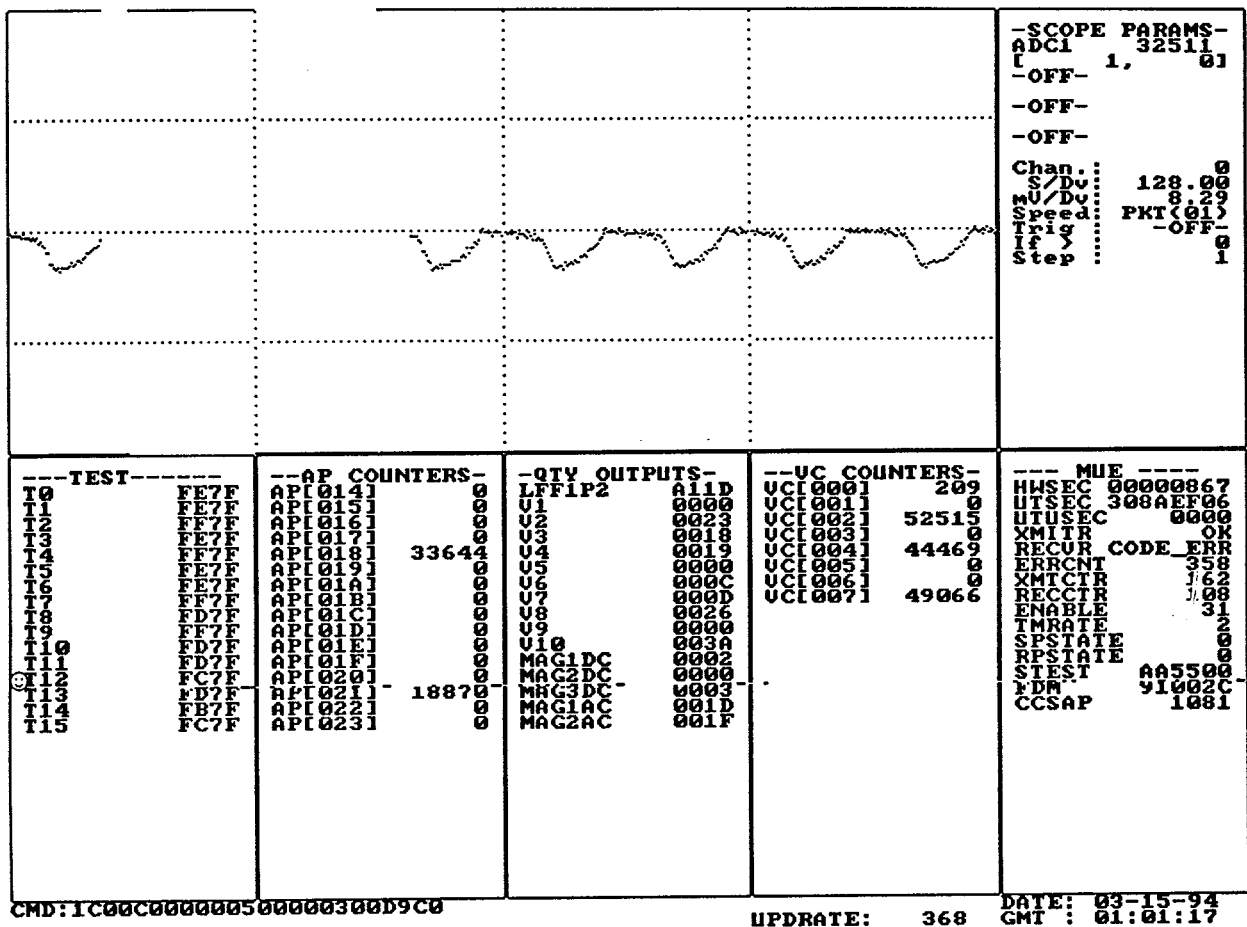
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GMT: 01:01:46

500Hz

Figure 19



500Hz

Figure 21

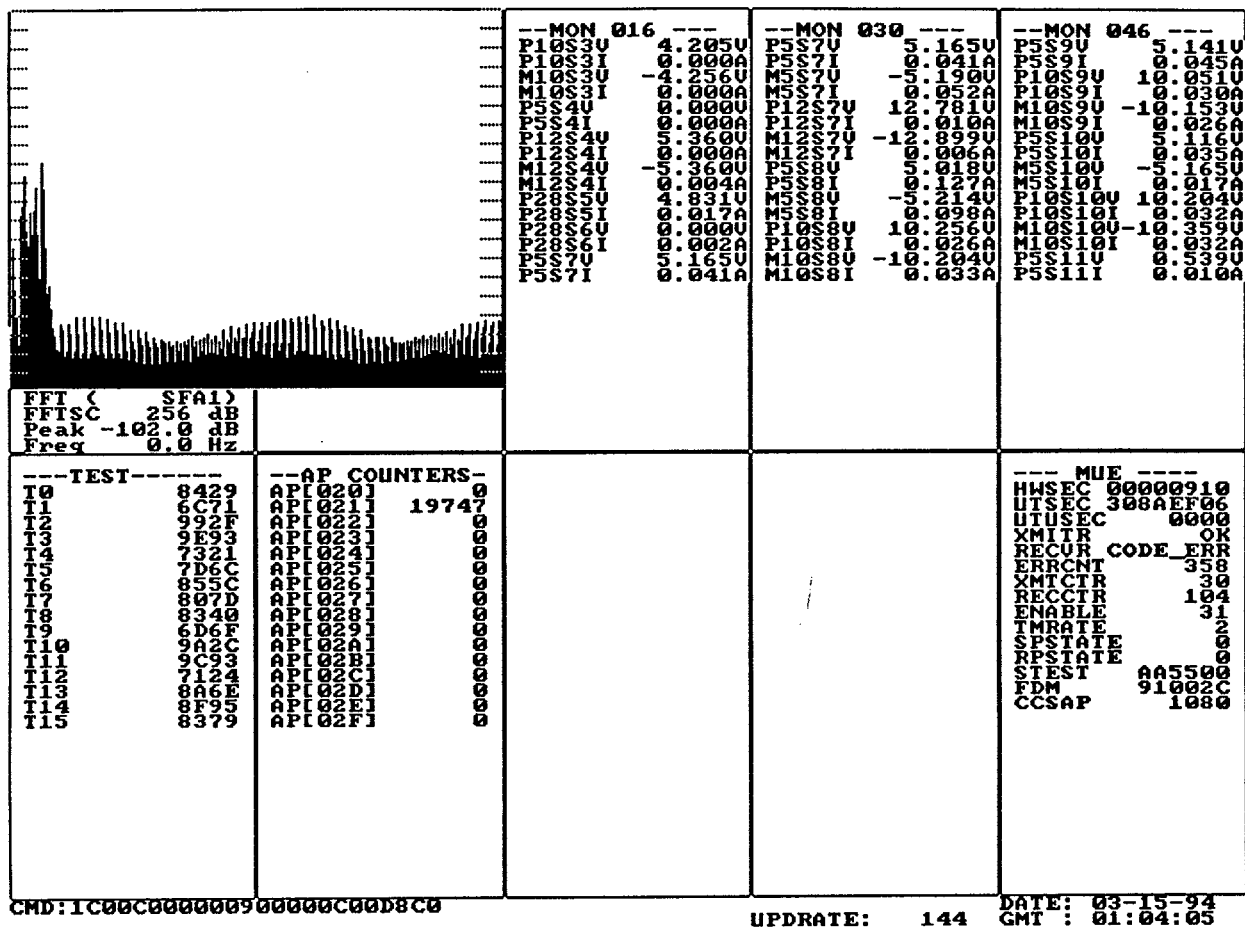
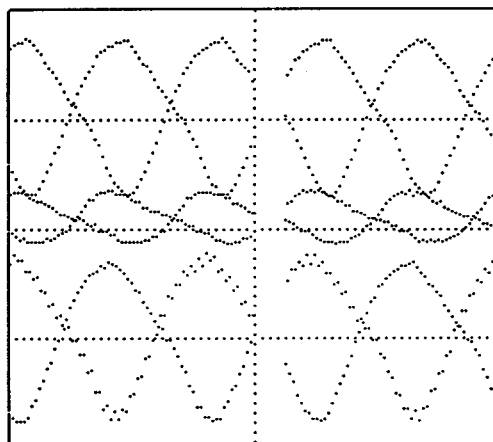


Figure 22

	<pre>-SCOPE PARAMS- U14 20 U58 1, 58] U910 1, 12] -OFF- 1, -58] Chan. S/Dv 128.00 MU/Dv 8.29 Speed PKT(01) Trig -OFF- Step 1</pre>	<pre>--MON 030 -- P5557U 4.99 P5557I 0.0334U M5557U 1.0000U M5557I 0.0000U P12557U 1.0000U P12557I 0.0000U M12557U 1.0000U M12557I 0.0000U P5557U 1.0000U P5557I 0.0000U M5557U 1.0000U M5557I 0.0000U P10557U 1.0000U P10557I 0.0000U M10557U 1.0000U M10557I 0.0000U</pre>	<pre>--MON 046 -- P5557U 5.092U P5557I 0.044U M5557U 1.0000U M5557I 0.0000U P10557U 1.0000U P10557I 0.0000U M10557U 1.0000U M10557I 0.0000U P5557U 1.0000U P5557I 0.0000U M5557U 1.0000U M5557I 0.0000U P10557U 1.0000U P10557I 0.0000U M10557U 1.0000U M10557I 0.0000U</pre>
<pre>---TEST--- T0 E97F T1 FF7F T2 DB7F T3 0280 T4 2580 T5 1380 T6 1780 T7 1380 T8 FF7F T9 DB7F T10 0280 T11 2580 T12 1380 T13 1780 T14 1380 T15 FF7F T16 DB7F T17 0280 T18 2580 T19 1380 T20 1780 T21 1380 T22 FF7F T23 DB7F T24 0280 T25 2580 T26 1380 T27 1780 T28 1380 T29 FF7F T30 DB7F T31 0280 T32 2580 T33 1380 T34 1780 T35 1380 T36 FF7F T37 DB7F T38 0280 T39 2580 T40 1380 T41 1780 T42 1380 T43 FF7F T44 DB7F T45 0280 T46 2580 T47 1380 T48 1780 T49 1380 T50 FF7F T51 DB7F T52 0280 T53 2580 T54 1380 T55 1780 T56 1380 T57 FF7F T58 DB7F T59 0280 T60 2580 T61 1380 T62 1780 T63 1380 T64 FF7F T65 DB7F T66 0280 T67 2580 T68 1380 T69 1780 T70 1380 T71 FF7F T72 DB7F T73 0280 T74 2580 T75 1380 T76 1780 T77 1380 T78 FF7F T79 DB7F T80 0280 T81 2580 T82 1380 T83 1780 T84 1380 T85 FF7F T86 DB7F T87 0280 T88 2580 T89 1380 T90 1780 T91 1380 T92 FF7F T93 DB7F T94 0280 T95 2580 T96 1380 T97 1780 T98 1380 T99 FF7F T100 DB7F</pre>	<pre>---AP COUNTERS--- AP[0000] 0 AP[0001] 0 AP[0002] 0 AP[0003] 0 AP[0004] 0 AP[0005] 0 AP[0006] 0 AP[0007] 0 AP[0008] 0 AP[0009] 0 AP[000A] 0 AP[000B] 0 AP[000C] 0 AP[000D] 0 AP[000E] 0 AP[000F] 0</pre>	<pre>--MON 096 -- P28S23U 0.0000U P28S23I 0.0001A P28S24U 7.316U P28S24I 0.0001A P28S25U 0.0000U P28S25I 0.0000U P28S26U 0.0000U P28S26I 0.0000U P28S27U 0.0000U P28S27I 0.0000U P28S28U 0.0000U P28S28I 0.0000U P28S29U 0.0000U P28S29I 0.0000U P28S30U 0.0000U P28S30I 0.0000U P28S31U 0.0000U P28S31I 0.0000U P28S32U 0.0000U P28S32I 0.0000U P28S33U 0.0000U P28S33I 0.0000U P28S34U 0.0000U P28S34I 0.0000U P28S35U 0.0000U P28S35I 0.0000U P28S36U 0.0000U P28S36I 0.0000U P28S37U 0.0000U P28S37I 0.0000U P28S38U 0.0000U P28S38I 0.0000U P28S39U 0.0000U P28S39I 0.0000U P28S40U 0.0000U P28S40I 0.0000U P28S41U 0.0000U P28S41I 0.0000U P28S42U 0.0000U P28S42I 0.0000U P28S43U 0.0000U P28S43I 0.0000U P28S44U 0.0000U P28S44I 0.0000U P28S45U 0.0000U P28S45I 0.0000U P28S46U 0.0000U P28S46I 0.0000U P28S47U 0.0000U P28S47I 0.0000U P28S48U 0.0000U P28S48I 0.0000U P28S49U 0.0000U P28S49I 0.0000U P28S50U 0.0000U P28S50I 0.0000U P28S51U 0.0000U P28S51I 0.0000U P28S52U 0.0000U P28S52I 0.0000U P28S53U 0.0000U P28S53I 0.0000U P28S54U 0.0000U P28S54I 0.0000U P28S55U 0.0000U P28S55I 0.0000U P28S56U 0.0000U P28S56I 0.0000U P28S57U 0.0000U P28S57I 0.0000U P28S58U 0.0000U P28S58I 0.0000U P28S59U 0.0000U P28S59I 0.0000U P28S60U 0.0000U P28S60I 0.0000U P28S61U 0.0000U P28S61I 0.0000U P28S62U 0.0000U P28S62I 0.0000U P28S63U 0.0000U P28S63I 0.0000U P28S64U 0.0000U P28S64I 0.0000U P28S65U 0.0000U P28S65I 0.0000U P28S66U 0.0000U P28S66I 0.0000U P28S67U 0.0000U P28S67I 0.0000U P28S68U 0.0000U P28S68I 0.0000U P28S69U 0.0000U P28S69I 0.0000U P28S70U 0.0000U P28S70I 0.0000U P28S71U 0.0000U P28S71I 0.0000U P28S72U 0.0000U P28S72I 0.0000U P28S73U 0.0000U P28S73I 0.0000U P28S74U 0.0000U P28S74I 0.0000U P28S75U 0.0000U P28S75I 0.0000U P28S76U 0.0000U P28S76I 0.0000U P28S77U 0.0000U P28S77I 0.0000U P28S78U 0.0000U P28S78I 0.0000U P28S79U 0.0000U P28S79I 0.0000U P28S80U 0.0000U P28S80I 0.0000U P28S81U 0.0000U P28S81I 0.0000U P28S82U 0.0000U P28S82I 0.0000U P28S83U 0.0000U P28S83I 0.0000U P28S84U 0.0000U P28S84I 0.0000U P28S85U 0.0000U P28S85I 0.0000U P28S86U 0.0000U P28S86I 0.0000U P28S87U 0.0000U P28S87I 0.0000U P28S88U 0.0000U P28S88I 0.0000U P28S89U 0.0000U P28S89I 0.0000U P28S90U 0.0000U P28S90I 0.0000U P28S91U 0.0000U P28S91I 0.0000U P28S92U 0.0000U P28S92I 0.0000U P28S93U 0.0000U P28S93I 0.0000U P28S94U 0.0000U P28S94I 0.0000U P28S95U 0.0000U P28S95I 0.0000U P28S96U 0.0000U P28S96I 0.0000U P28S97U 0.0000U P28S97I 0.0000U P28S98U 0.0000U P28S98I 0.0000U P28S99U 0.0000U P28S99I 0.0000U P28S100U 0.0000U P28S100I 0.0000U</pre>	<pre>---MUE --- HWSEC 00000970 UTISEC 308AEF06 UTUSEC 0000 XMITR OK RECUR CODE ERR ERRCNT 358 XMITCTR 434 RECCTR 108 ENABLE 31 TMRATE 0 SPSTATE 0 RPSTATE 0 TEST AA3500 FDM 91002C CCSAP 1080</pre>

CMD:IC00C000001D00000000A8CD

UPDRATE: 160 DATE: 03-15-94
GMT: 01:05:41

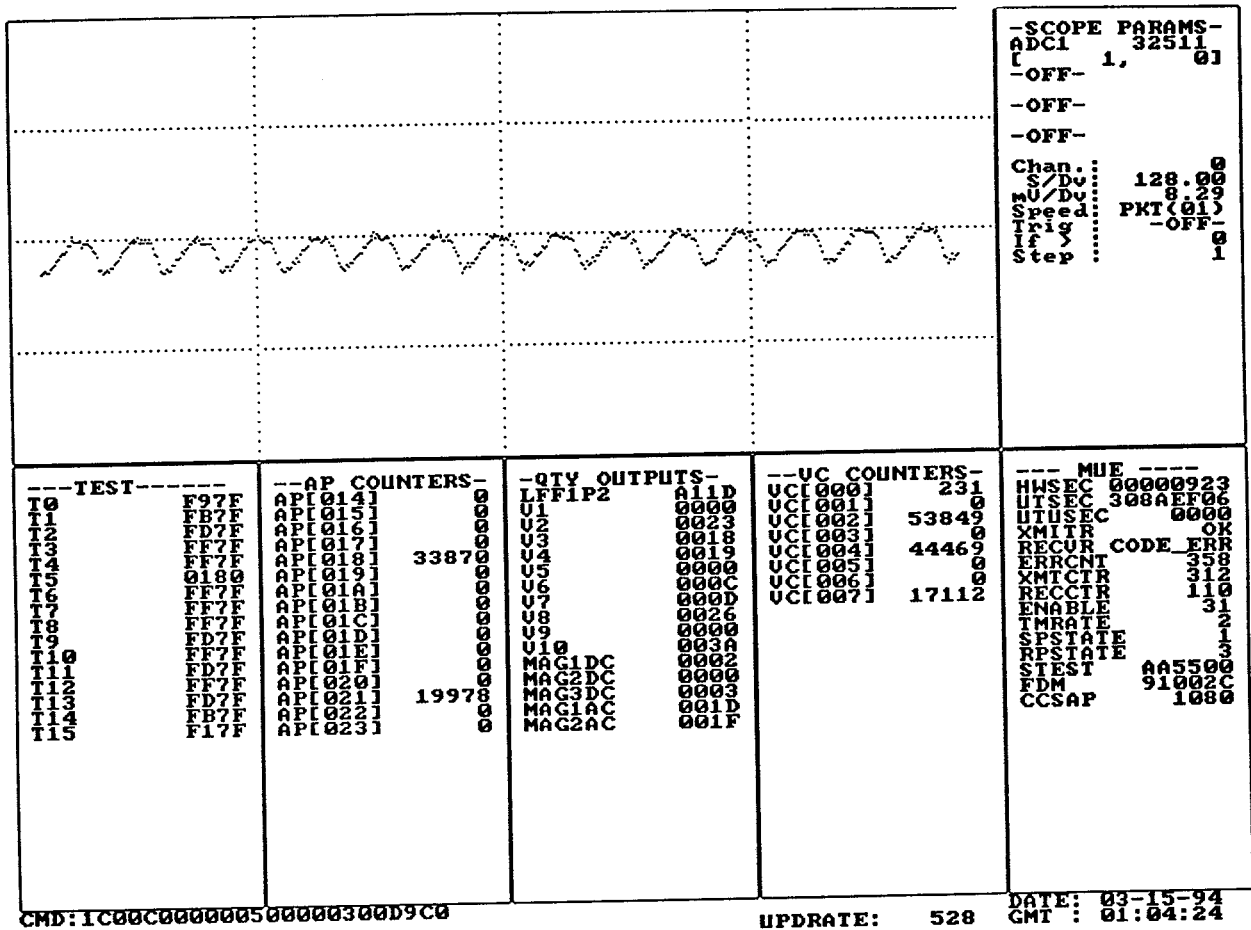
1KHz

[illegible]

UPDATE: 416 DATE: 03-15-94
GMT : 01:06:59

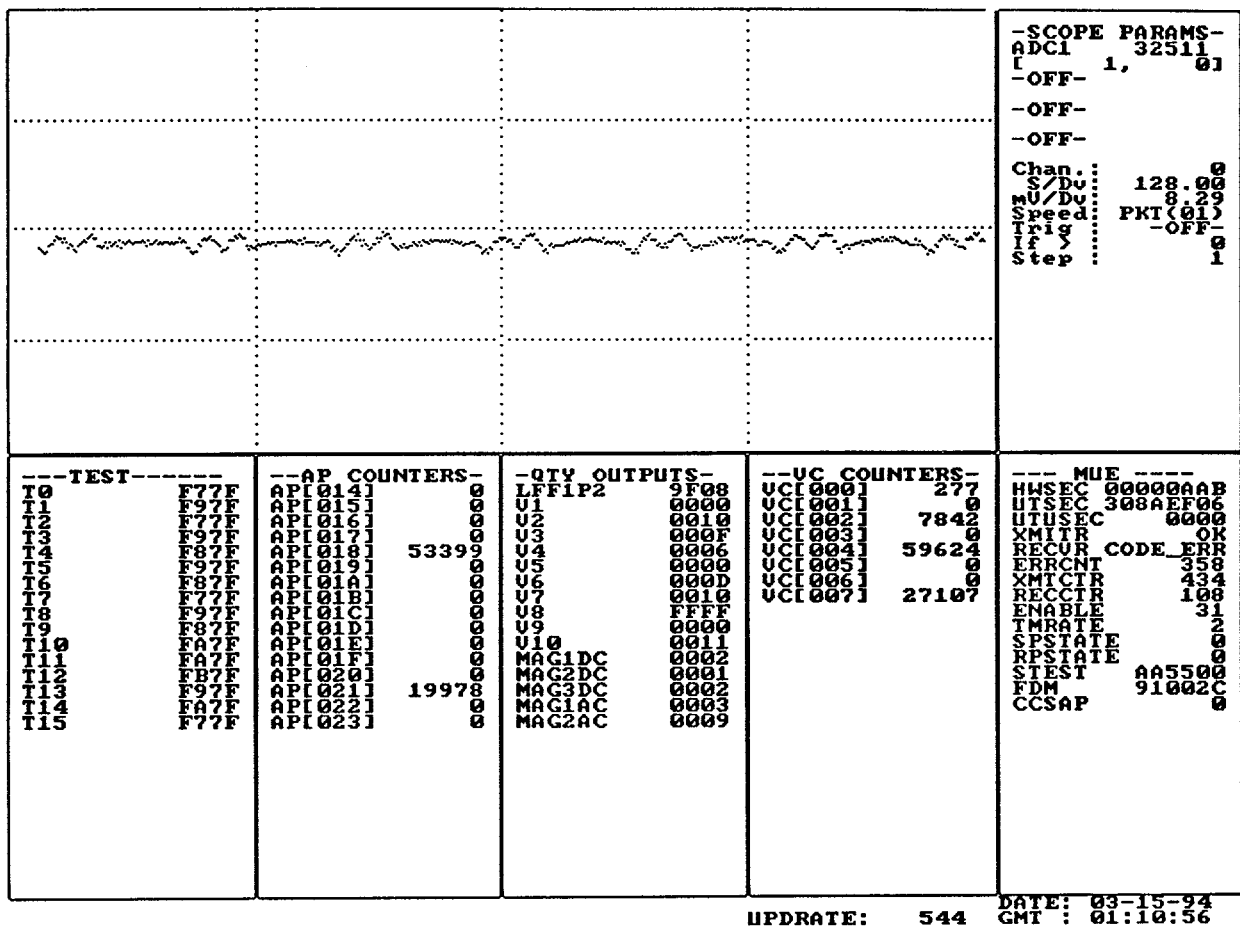
1 kHz

Figure 27



1/21/73

FIGURE 25



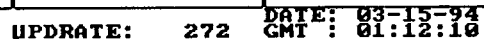
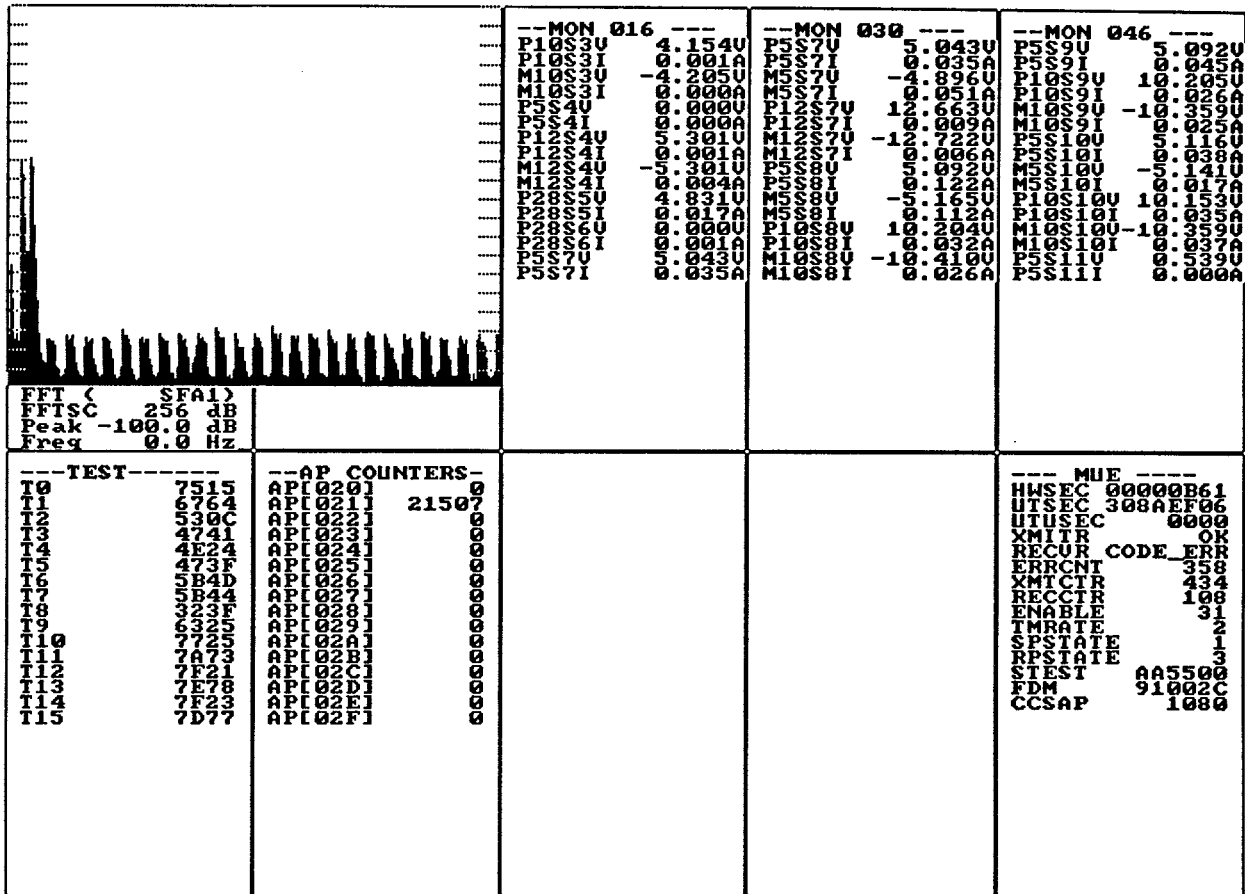


Figure 28



				-SCOPE PARAMS- ADC1 32511 1 01 -OFF- -OFF- -OFF- Chan. S/Dc 128 00 M U/Dc 8 29 Speed PKT(01) Trig -OFF- If 0 Step 1
---TEST--- T10 F97F T11 FA7F T12 F87F T13 F87F T14 F77F T15 F37F T16 F87F T17 F87F T18 FB7F T19 FA7F T110 F87F T111 F97F T112 F97F T113 F77F T114 F77F T115 F97F	--AP COUNTERS-- AP10141 0 AP10151 0 AP10161 0 AP10171 0 AP10181 53706 AP10191 0 AP101A1 0 AP101B1 0 AP101C1 0 AP101D1 0 AP101E1 0 AP101F1 0 AP10201 21712 AP10211 0 AP10221 0 AP10231 0	-QTY OUTPUTS- LFF1P2 9F08 U1 0000 U2 0000 U3 000F U4 0006 U5 0000 U6 0000 U7 0010 U8 FFFF U9 0000 V10 0011 MAC1DC 0002 MAC2DC 0001 MAC3DC 0002 MAC1AC 0003 MAC2AC 0009	--UC COUNTERS-- UC10001 299 UC10011 0 UC10021 9883 UC10031 0 UC10041 59624 UC10051 0 UC10061 0 UC10071 9327	---MUE--- HWSEC 00000B7B UTSEC 308AET06 UTUSEC 0000 XMITR OK RECUR CODE ERR ERRCNT 358 XMITCTR 434 RECCTR 108 ENABLE 31 TMRATE 2 SPSTATE 1 RPSTATE 3 STEST AA5500 FDM 91002C CCSAP 1083

CMD:IC00C000000500000300D9C0

UPDRATE: 368 DATE: 03-15-94
GMT: 01:14:24

46Hz

		<pre>--SCOPE PARAMS-- U14 5 U58 1, 581 U910 1, 61 1, 101 1, -581 --OFF-- Chan..... 2 S/Dv..... 128.00 M/Dv..... 8.29 Speed..... PKT(01) Trig..... -OFF- If..... 0 Step..... 1</pre>	<pre>--MON 030-- P5000VU 0.043V P5000VU 0.043V P5000VU 0.043V M5000VU 0.043V P1000VU 0.043V P1000VU 0.043V P1000VU 0.043V M1000VU 0.043V P5000VU 0.043V P5000VU 0.043V M5000VU 0.043V M5000VU 0.043V P1000VU 0.043V P1000VU 0.043V P1000VU 0.043V M1000VU 0.043V P5000VU 0.043V P5000VU 0.043V</pre>	<pre>--MON 046-- P5000VU 0.043V P5000VU 0.043V P1000VU 0.043V P1000VU 0.043V M1000VU 0.043V P5000VU 0.043V M5000VU 0.043V M5000VU 0.043V P1000VU 0.043V P1000VU 0.043V P1000VU 0.043V M1000VU 0.043V P5000VU 0.043V P5000VU 0.043V</pre>
<pre>--TEST-- T0 0380 T1 0380 T2 0380 T3 0380 T4 0680 T5 0780 T6 0780 T7 0180 T8 0580 T9 0780 T10 0380 T11 0380 T12 0680 T13 0680 T14 0680 T15 27BD</pre>	<pre>--AP COUNTERS-- AP1000VU 0 AP1</pre>			

CMD:1C00C000001D00000000A8CD

UPDRATE: 160 DATE: 03-15-94 GMT: 01:16:01

8 k43

				-SCOPE PARAMS- ADC1 1, 32511 -OFF- -OFF- -OFF- Chan.: S/Dv: 128.00 M/Dv: 8.29 Speed: PMT(01) Trig: -OFF- If: 0 Step: 1
---TEST--- T0 F97F T1 F97F T2 F77F T3 F77F T4 F97F T5 F97F T6 F97F T7 F97F T8 F97F T9 F97F T10 F97F T11 F97F T12 F97F T13 F97F T14 F97F T15 F97F	---AP COUNTERS--- AP10141 0 AP10151 0 AP10161 0 AP10171 0 AP10181 59552 AP10191 0 AP101A1 0 AP101B1 0 AP101C1 0 AP101D1 0 AP101E1 0 AP101F1 0 AP10201 0 AP10211 21712 AP10221 0 AP10231 0	---QTY OUTPUTS--- LFF1P2 A30D U1 0000 U2 0000 U3 0007 U4 0004 U5 FFFE U6 0000 U7 0000 U8 0009 U9 FFFF U10 FFFF MAG1DC 0003 MAG2DC 0001 MAG3DC 0003 MAG1AC FFFF MAG2AC 0003	---UC COUNTERS--- UCI0001 310 UCI0011 0 UCI0021 15729 UCI0031 0 UCI0041 63474 UCI0051 0 UCI0061 13807 UCI0071 0	---MUE--- HWSEC 00000BF3 UTSEC 308AEF06 UTUSEC 0000 XMITR OK RECUR CODE_ERR ERRCNT 358 XMITCTR 434 RECCTR 108 ENABLE 31 TMRATE 2 SPSTATE 1 RPSTATE 3 STEST AA5500 FDM 91002C CCSAP 1084

CMD: 1C00C000000500000300D9C0

UPDATE: 352 DATE: 03-15-94
GMT: 01:16:24

8k4

				-SCOPE PARAMS- ADC1 1, 32511 -OFF- -OFF- -OFF- Chan.: 0 S/Du: 128.00 MU/Du: 8.29 Speed: PKT(01) Trig: -OFF- If: 0 Step: 1
---TEST--- T0 F77F T1 FA7F T2 F97F T3 F87F T4 F87F T5 F97F T6 F97F T7 F77F T8 F97F T9 F97F T10 FA7F T11 F87F T12 F87F T13 F87F T14 F87F T15 F97F	---AP COUNTERS--- AP[014] 0 AP[015] 0 AP[016] 0 AP[017] 0 AP[018] 6246 AP[019] 0 AP[01A] 0 AP[01B] 0 AP[01C] 0 AP[01D] 0 AP[01E] 0 AP[01F] 0 AP[020] 0 AP[021] 2351 AP[022] 0 AP[023] 0	---QTY OUTPUTS--- LFFIP2 030E U1 0000 U2 0006 U3 0002 U4 0004 U5 0000 U6 0003 U7 0008 U8 FFFF U9 0000 U10 0006 MACC1D 0003 MACC2D 0001 MACC3D 0003 MACC4D 0003 MACC5D 0004	---UC COUNTERS--- UCI000] 334 UCI001] 0 UCI002] 20441 UCI003] 0 UCI004] 64386 UCI005] 0 UCI006] 0 UCI007] 64831	---MUE--- HWSEC 00000CF8 UTSEC 308AEF06 UTUSEC 0000 XMITR OK RECUR CODE_ERR ERRCNT 358 XMICTR 434 RECCTR 108 ENABLE 31 TMRATE 2 SPSTATE 0 RPSTATE 3 STEST AA5500 FDM 91002C CCSAP 1085

CMD:1C00C000000500000300D9C0

UPDATE: 352 DATE: 03-15-94
GMT: 01:20:44

50kH

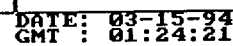


Figure 38

				-SCOPE PARAMS- ADC1 1, 32511 -OFF- 01 -OFF- -OFF- Chan. 0 S/Dv 128.00 MU/Dv 8.29 Speed PKT(01) Trig -OFF- If > 0 Step 1
---TEST--- T0 F77F T1 FA7F T2 FB7F T3 F87F T4 F97F T5 F77F T6 F77F T7 F87F T8 F97F T9 FA7F T10 FA7F T11 F87F T12 F67F T13 F97F T14 F97F T15 FB7F	--AP COUNTERS-- AP[014] 0 AP[015] 0 AP[016] 0 AP[017] 0 AP[018] 63056 AP[019] 0 AP[01A] 0 AP[01B] 0 AP[01C] 0 AP[01D] 0 AP[01E] 0 AP[01F] 0 AP[020] 0 AP[021] 23514 AP[022] 0 AP[023] 0	--QTY OUTPUTS-- LFF1P2 030E U1 000000 U2 000000 U3 000000 U4 000000 U5 000000 U6 000000 U7 000000 U8 000000 U9 000000 U10 000000 MAG1DC 0003 MAG2DC 0001 MAG3DC 0003 MAG1AC 0003 MAG2AC 0004	--UC COUNTERS-- UCI0001 334 UCI0011 0 UCI0021 21035 UCI0031 0 UCI0041 64386 UCI0051 0 UCI0061 0 UCI0071 18279	--- MUE --- HMSEC 00000D5E UTSEC 308AEF06 UTUSEC 0000 XMITR OK RECUR CODE_ERR ERRCNT 358 XMTCTR 232 RECCTR 106 ENABLE 31 TMRATE 2 SPSTATE 0 RPSTATE 0 STEST AA5500 FDM 91002C CCSAP 1082
CMD:1C00C000000500000300D9C0				UPDATE: 368 DATE: 03-15-94 GMT: 01:22:27

100 kHz

Figure 39

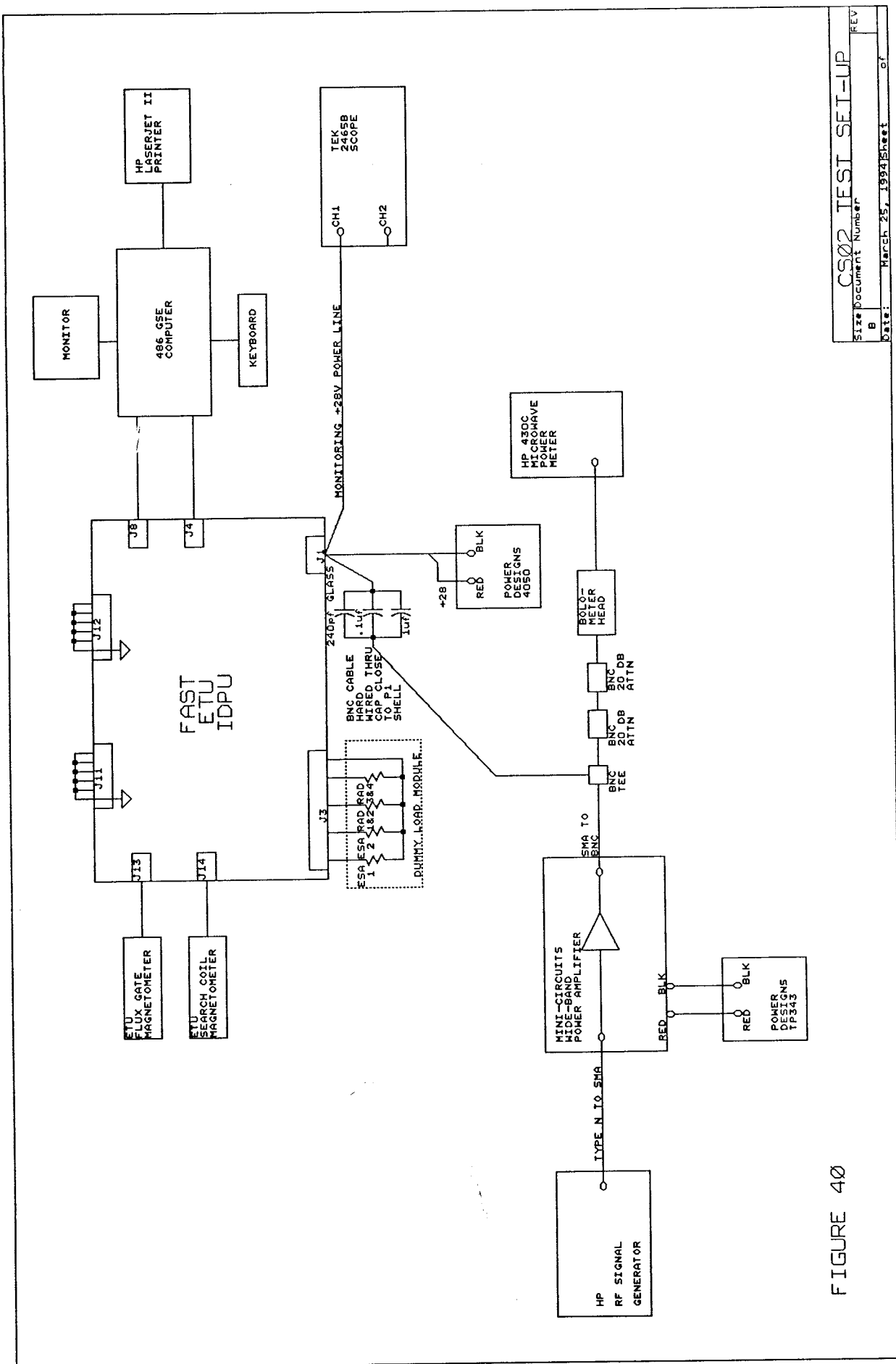
CS02 TEST PROCEDURE AND RESULTS

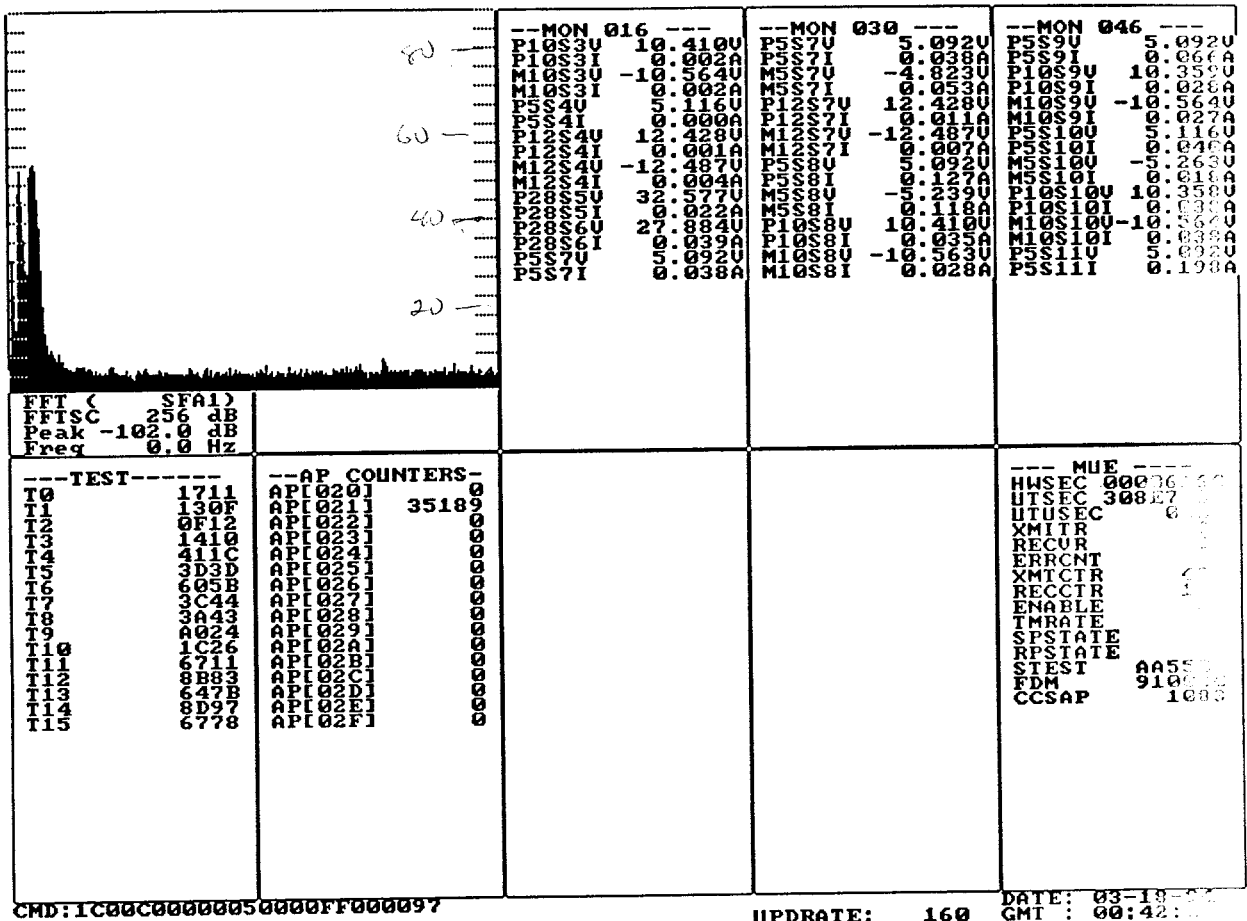
Figure 40 shows the set-up used to perform the CS02 test for the frequency range from 500 kHz to 400 MHz. For the frequency range from 50 to 500 kHz, the HP 3313A wave form generator was used in place of the RF signal generator and power amplifier.

The test procedure consisted in running the GSE scripts TVSFA, TVADC1, TVSVY0, and SHOMON2 to look for interference in the E-Field and processor systems, while sweeping thru the 50 kHz to 400 MHz frequency range. The test requirement is that the signal amplitude be maintained at 1 volt rms across the input power line of the test sample, limited by what the signal generator can deliver across the test load, subject to the requirement that the generator be capable of putting 7.07 volts rms across a 50 ohm load. Due to a mis-reading of the requirement, the test was run at a level of 7 volts rms instead of 1 volt rms. The results of this testing are shown in Figures 41 thru 70.

The CS02 testing gave little if any interference in the ADC, SVY, or the monitor functions, but it did have an effect in the SFA channels (which are the most sensitive in the instrument). For this reason, only the SFA data are included in the figures. Significant interference occurred at some 12 rather sharp peaks in frequency starting at 6.38 MHz and going up to 180.93 MHz. The the strongest of these were odd multiples of the 10.7 MHz IF frequency used in the SFA. In between these peaks the interference was less than at the peak on either side. Two points were run at the correct value of 1 volt rms instead of 7.07 volts rms. The result of this was to reduce the level of the interference in the SFA by the same amount, i.e. 17 dB, or about 4 divisions on the SFA plots given.

Given this reduction from the plots given, and the narrow width in frequency where the interference occurred, we conclude that the disturbance is of a relatively low level and if it were to occur in flight would have a tolerable impact on the science data.

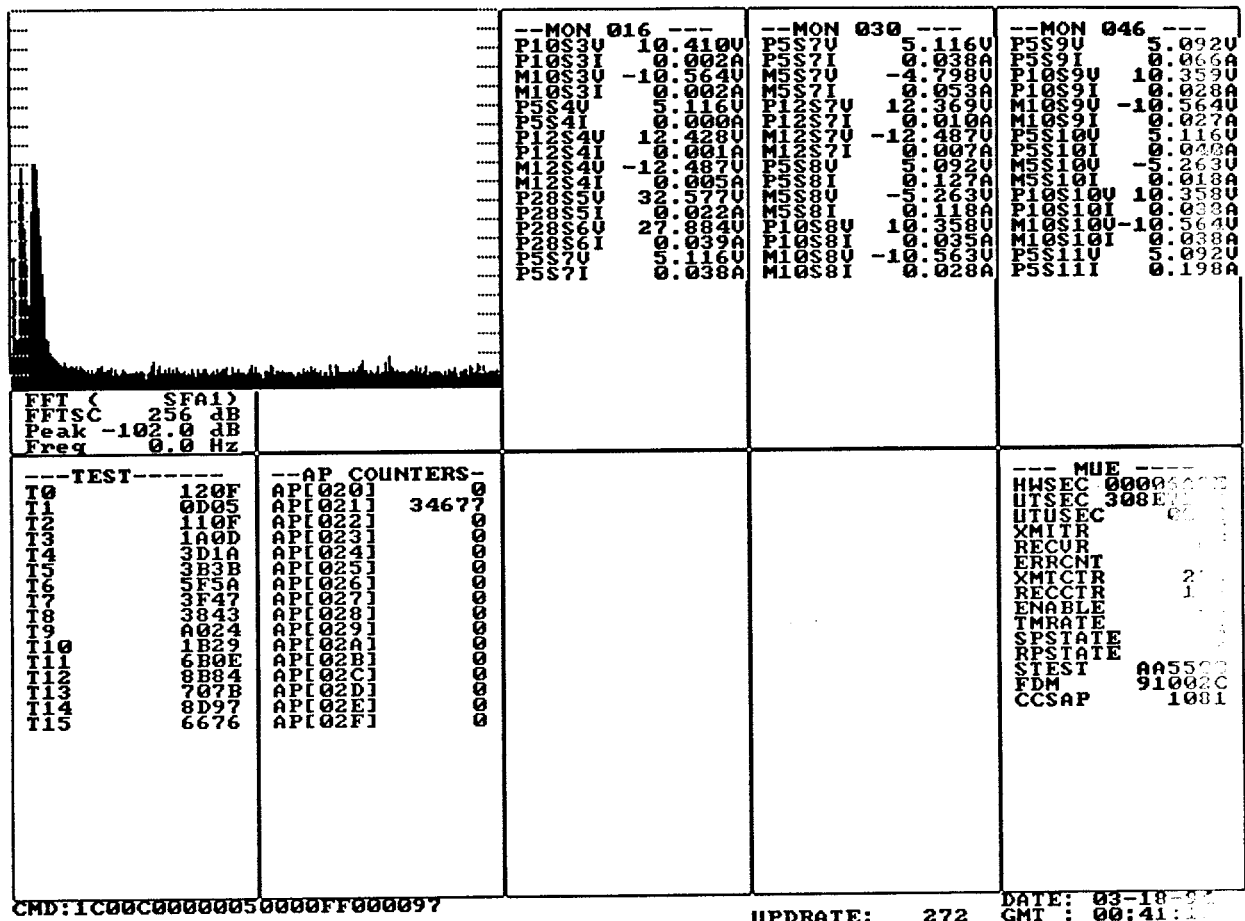




CS02
50 kHz

MAR 18 1994

Figure 41

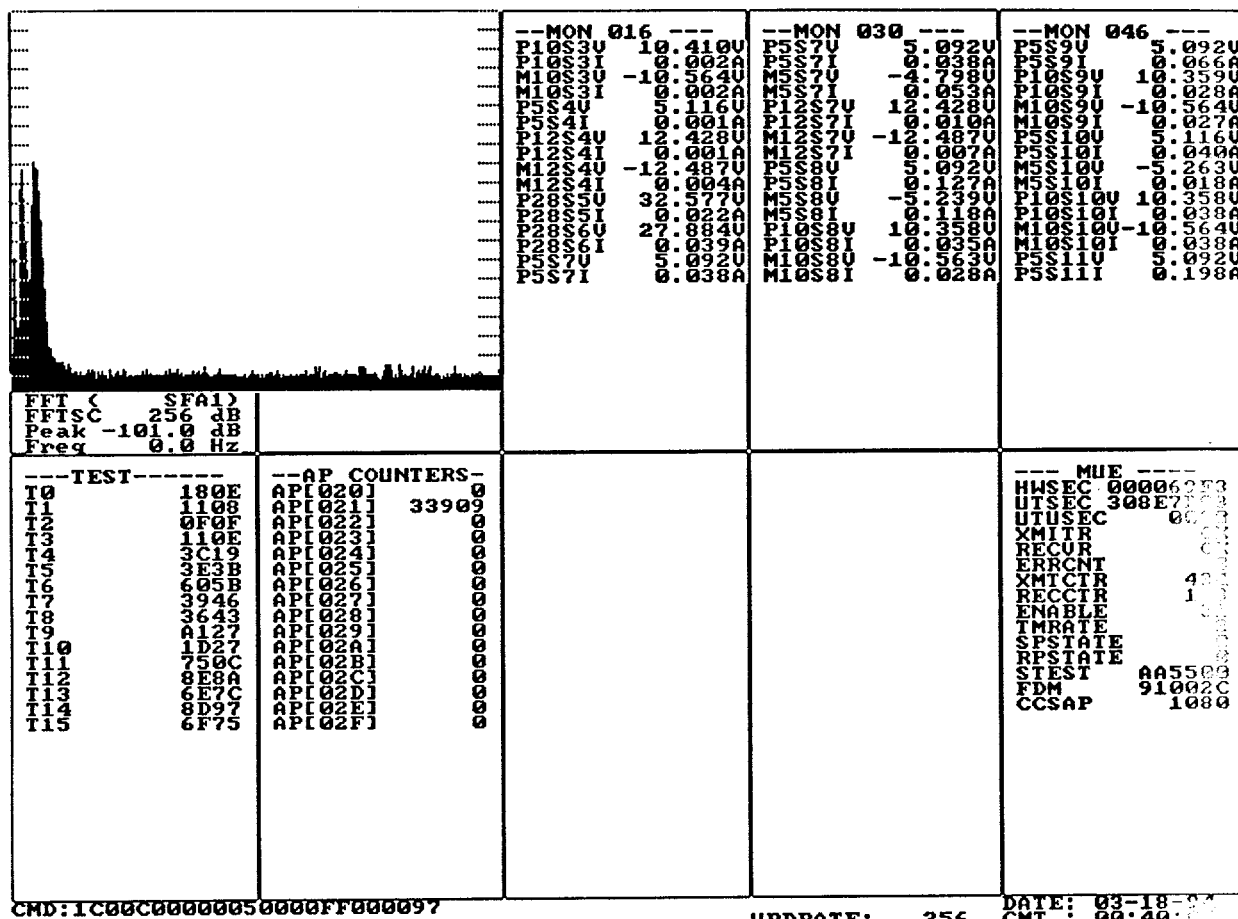


C502

100 KHz

MAR 18 1994

Figure 42

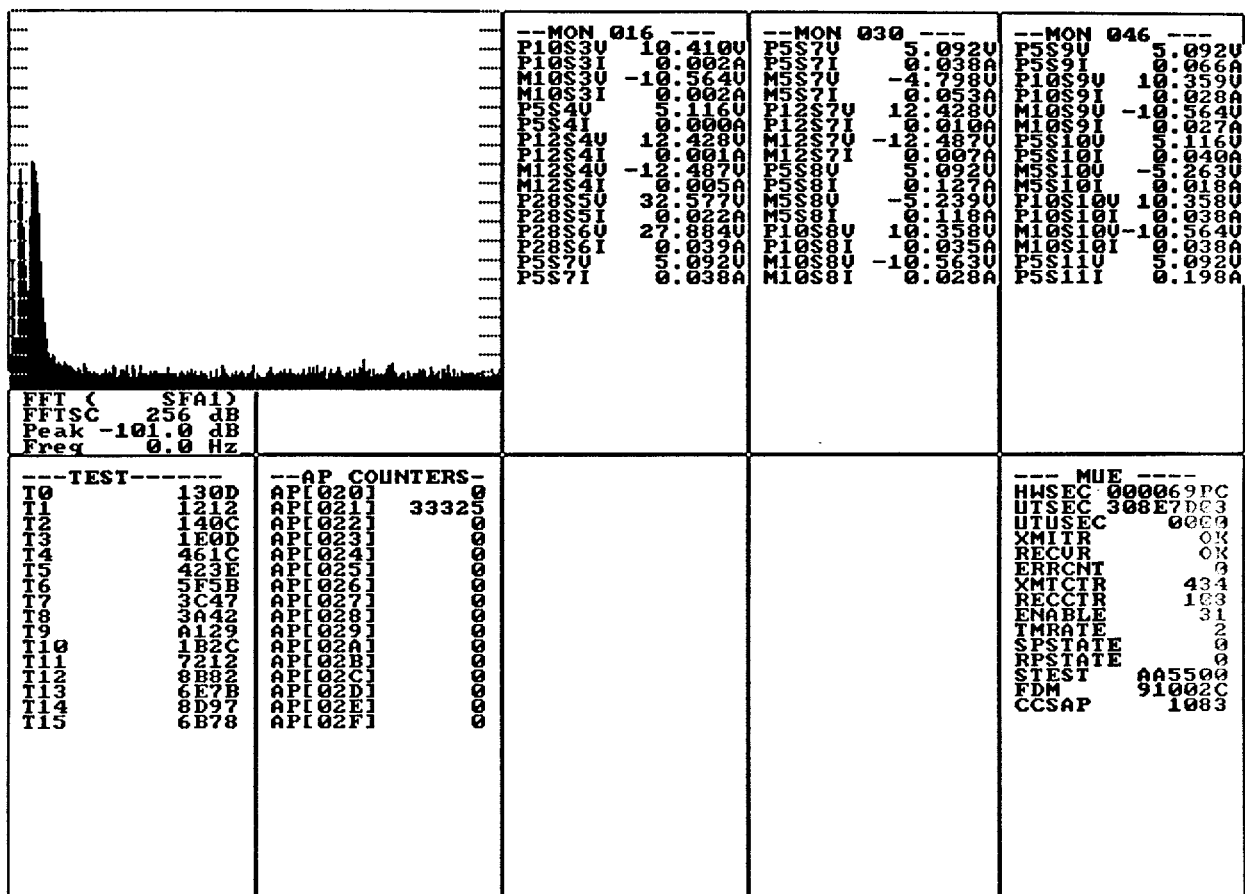


CS02

300 kHz

MAR 18 1994

Figure 43



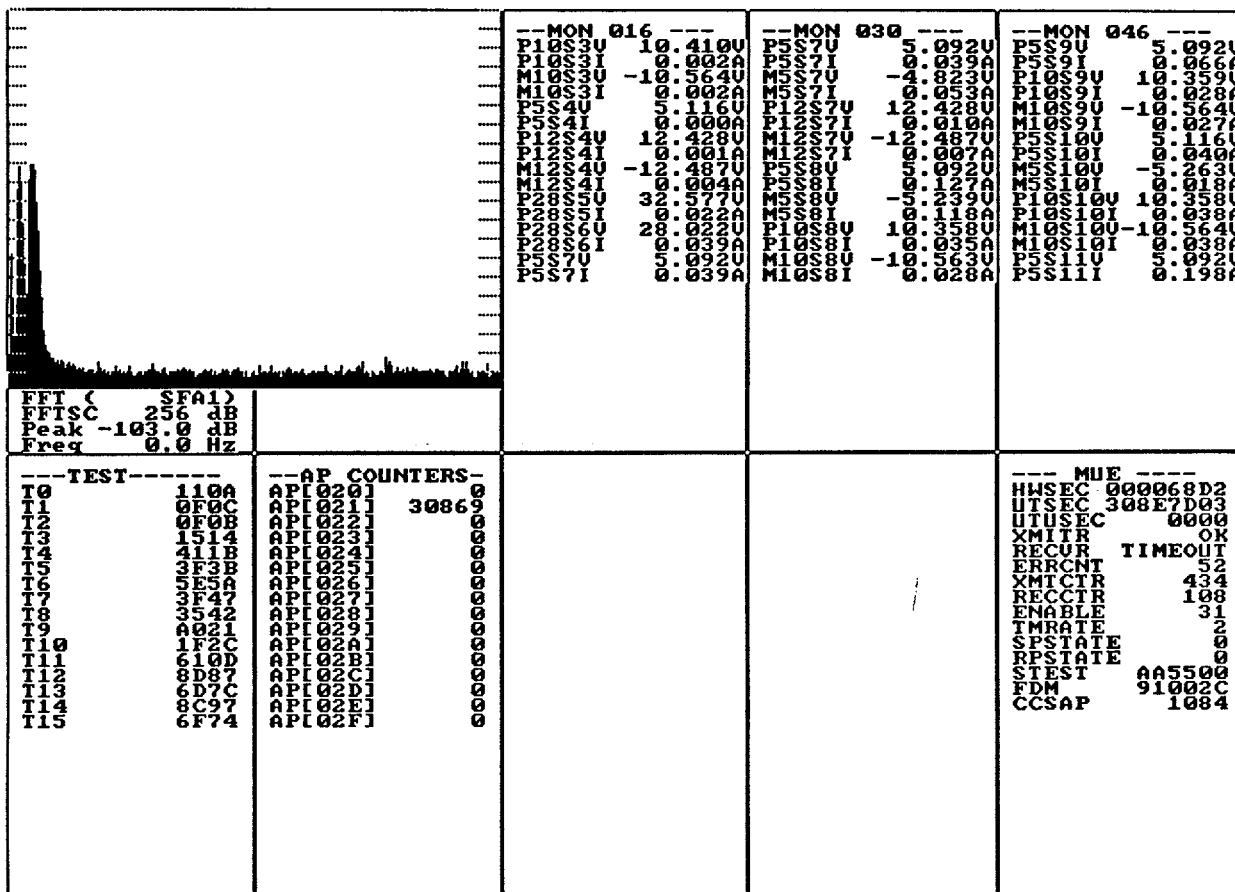
CMD:1C00C00000050000FF000097

UPDATE: 160 DATE: 03-18-94 GMT: 00:39:05

C502
500KHz

MAR 18 1994

Figure 44



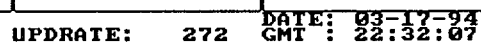
CMD:1C00C00000050000FF000097

UPDRATE: 256 DATE: 03-18-94
 GMT: 00:35:11

1mHz
 CS02

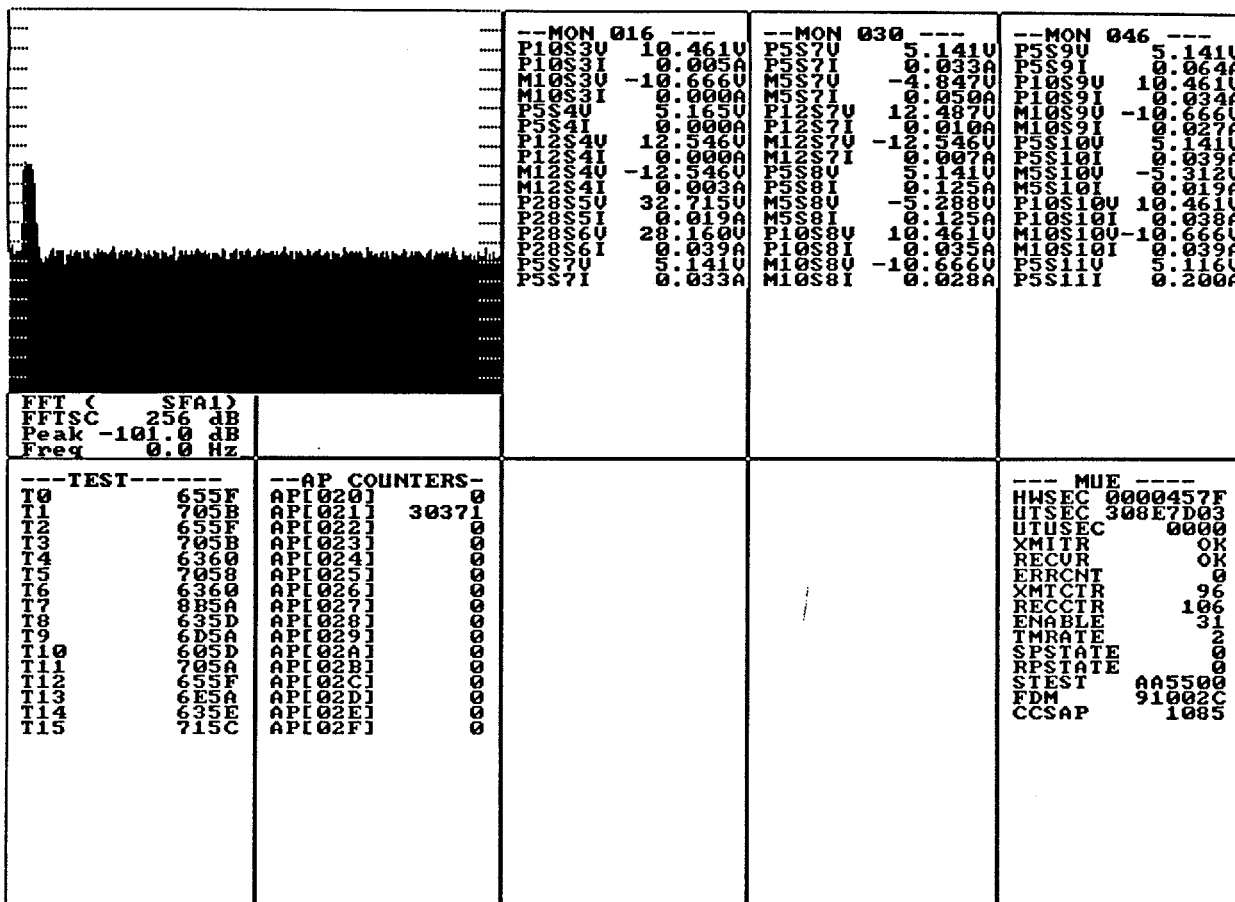
MAR 18 1994

FIGURE 45



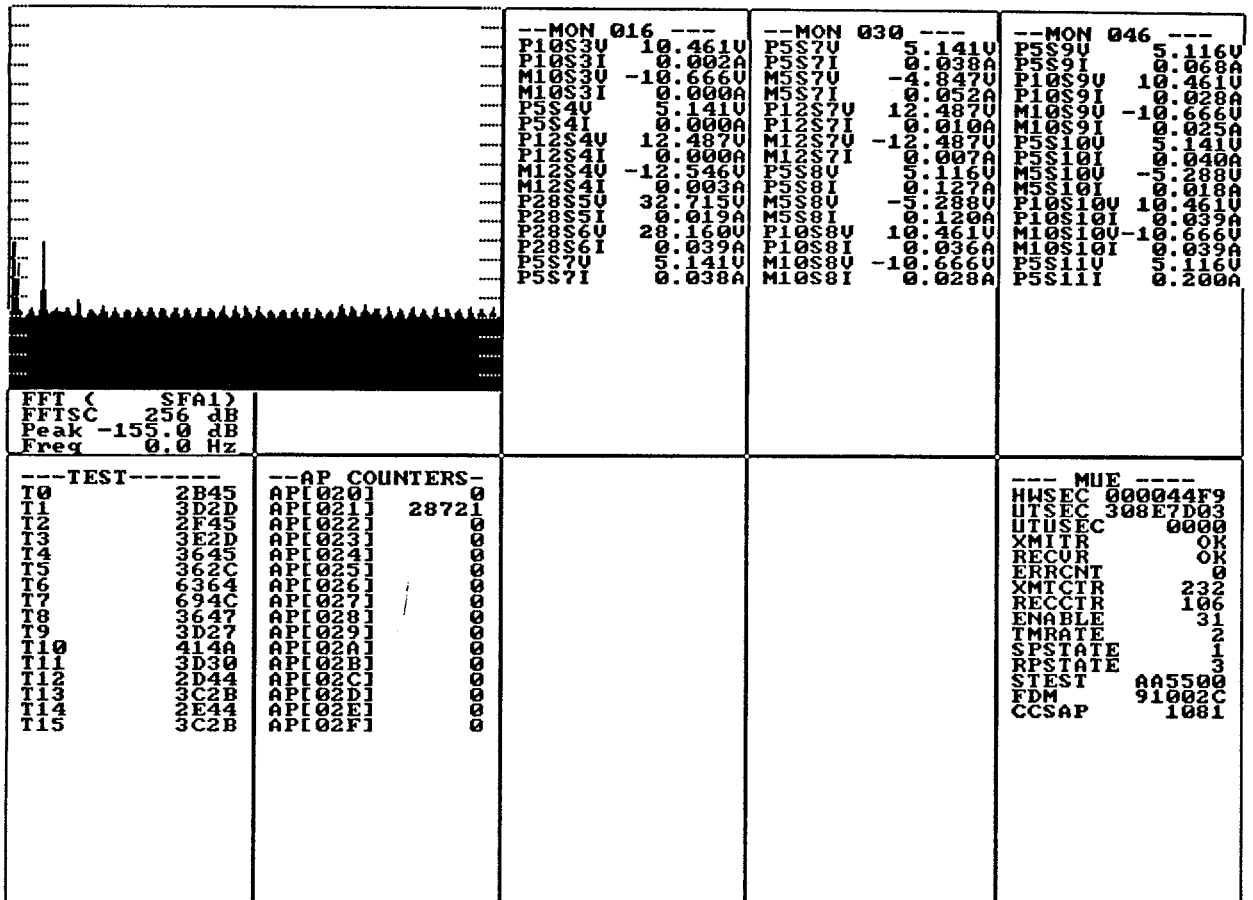
1502

Figure 46



UPDATE: 32 DATE: 03-17-94
GMT: 22:04:30

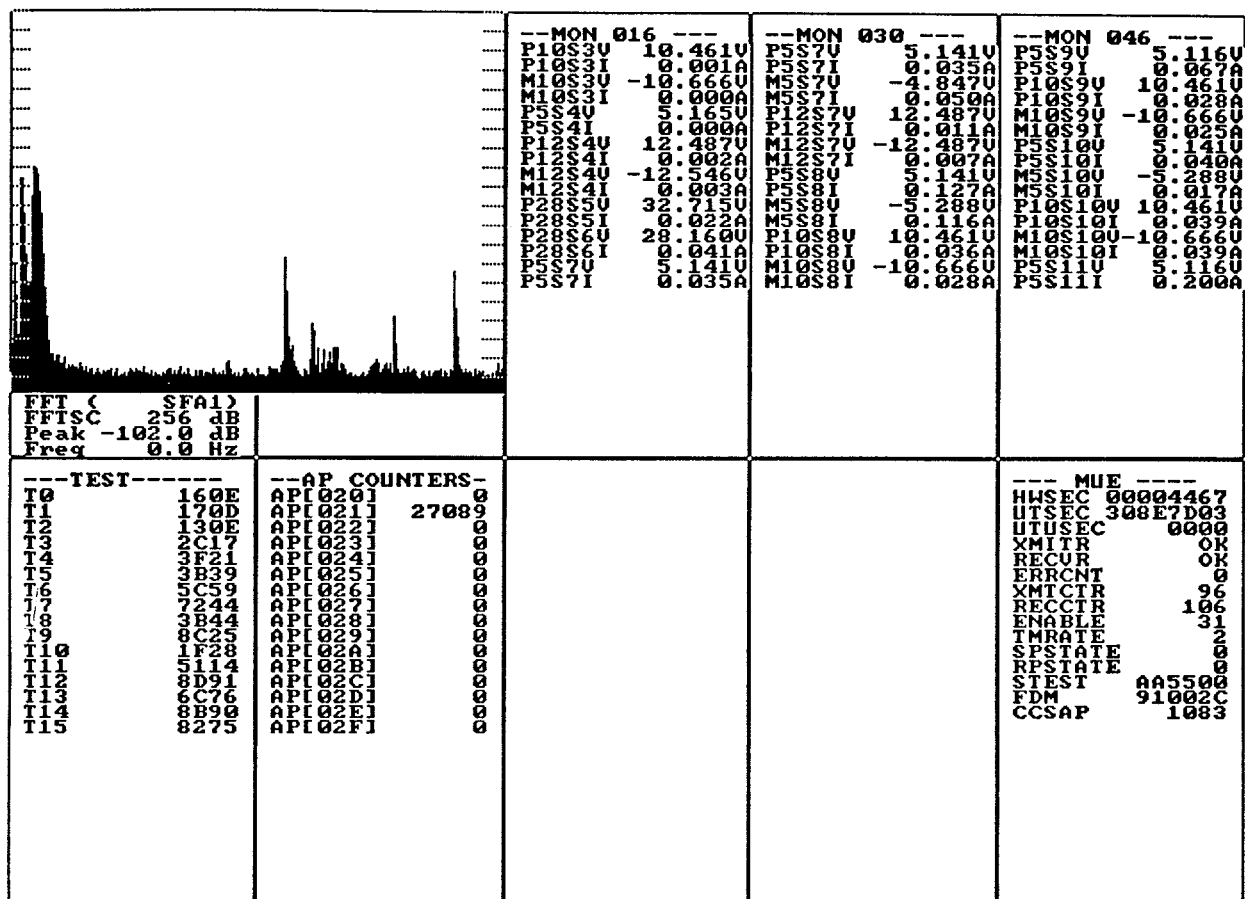
MAR 18 1994
31.946 W 413
CS02



UPDATE: 272 DATE: 03-17-94 GMT: 22:02:16

C502
42.704 m

MAR 1994



CMD:IC00C00000050000FF000097

UPDRATE: 160 DATE: 03-17-94 GMT: 21:59:50

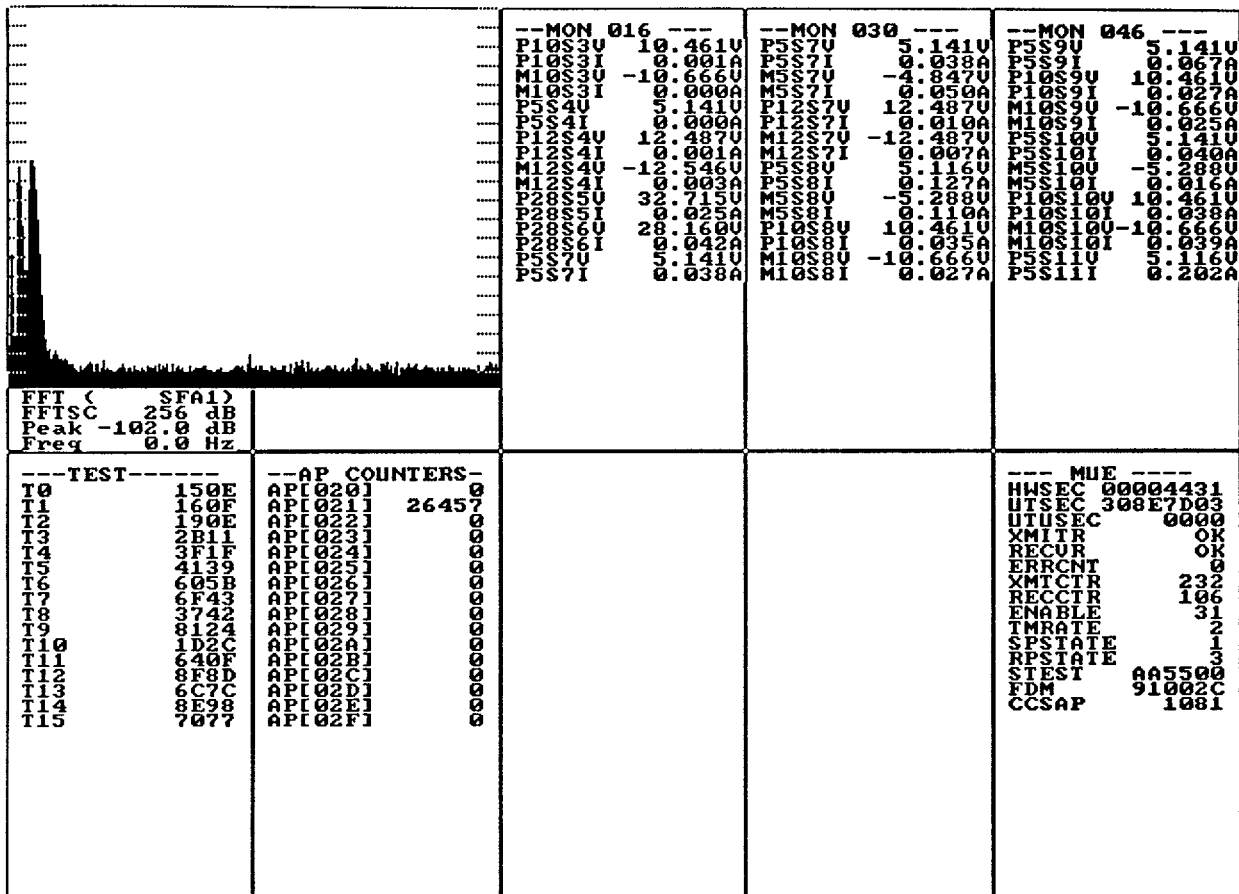
MAR 18 1994

45.4984

44.27

C502

Figure 54



CMD:1C00C00000050000FF000097

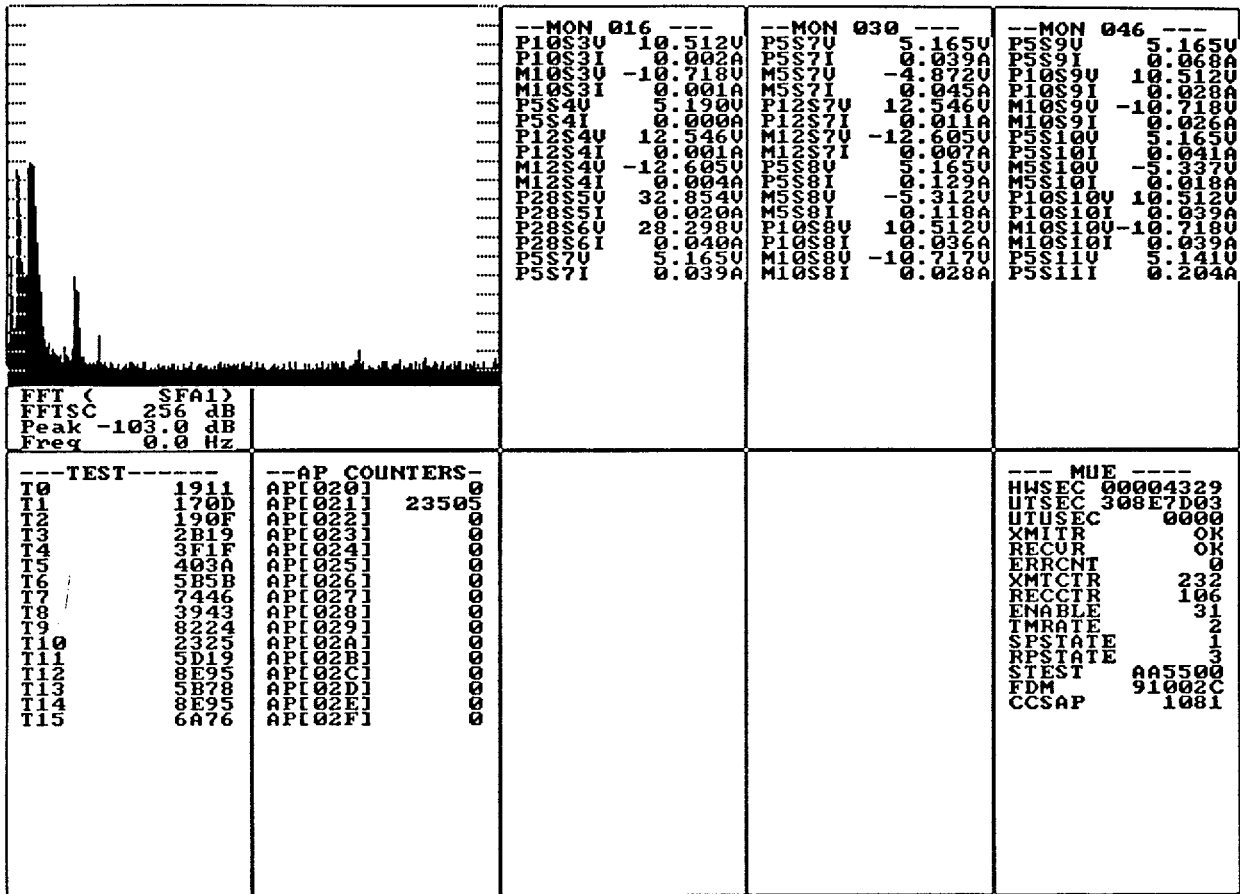
UPDRATE: 256 DATE: 03-17-94 GMT: 21:58:56

MAR 18 1994

50 mhz
CS02

Figure

55

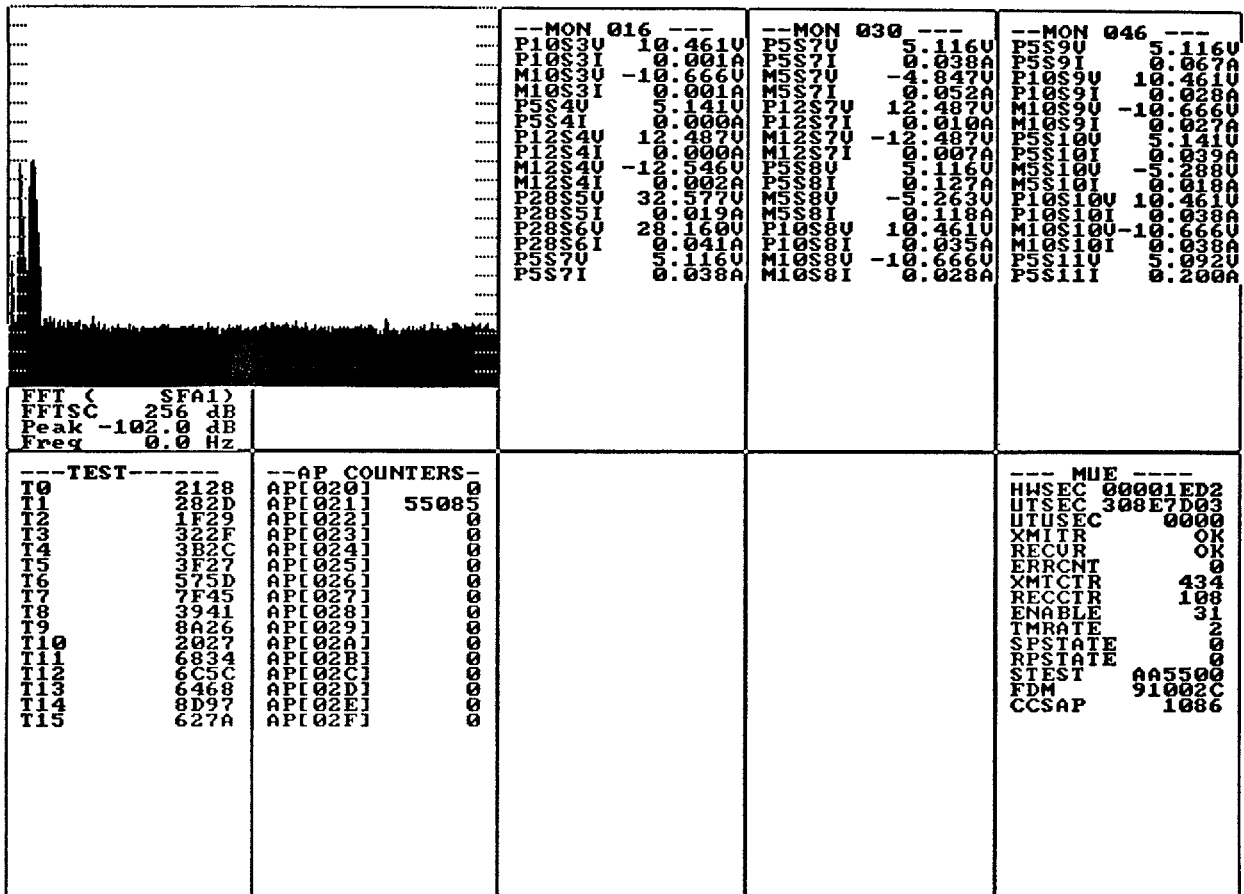


CMD:1C00C00000050000FF000097

UPDRATE: 272 DATE: 03-17-94 GMT: 21:54:32

MAR 18 1994
 64.815 mhz
 CSO 2

FIGURE 56



CMD: 1C00C00000050000FF000097

UPDRATE: 272 DATE: 03-17-94 GMT: 19:19:31

MAR 18 1994
106.403
CS02

Figure 59

		MON 016 P10003U 10.461U P10003I 0.0001A M10003U -10.666U P5004U 5.141U P10004I 0.000A P12004U 12.487U P12004I -12.487U M12004U -12.487U P20000U 32.577U P20000I 20.002U P5007U 5.116U P5007I 6.638A	MON 030 P5007U 5.116U P5007I 0.038A M5007U -4.847U P12007U 12.428U P12007I -12.487U M12007U -12.487U P5008U 5.116U P5008I 0.007A M5008U 1.166U P10008U 1.166U P10008I 1.166U M10008U 1.166U P5008I 6.666U M10008I 6.628A	MON 046 P5009U 5.116U P5009I 0.067A M10009U 10.410U M10009U -10.666U P5009U 5.141U P5009I -5.041U M5009U 5.288U P10009U 1.166U P10009I -1.166U M10009U 1.166U P5009I 6.666U M10009I 6.210A
FFT (SFA1) FFTSC 256 dB Peak -100.0 dB Freq 0.0 Hz				
TEST T0 474E T1 1055U T2 474E T3 224E T4 474E T5 224E T6 474E T7 474E T8 474E T9 474E T10 474E T11 474E T12 474E T13 474E T14 474E T15 474E		AP COUNTERS AP1 0 AP2 0 AP3 0 AP4 0 AP5 0 AP6 0 AP7 0 AP8 0 AP9 0 AP10 0 AP11 0 AP12 0 AP13 0 AP14 0 AP15 0 AP16 0 AP17 0 AP18 0 AP19 0 AP20 0 AP21 0 AP22 0 AP23 0 AP24 0 AP25 0 AP26 0 AP27 0 AP28 0 AP29 0 AP30 0 AP31 0 AP32 0 AP33 0 AP34 0 AP35 0 AP36 0 AP37 0 AP38 0 AP39 0 AP40 0 AP41 0 AP42 0 AP43 0 AP44 0 AP45 0 AP46 0 AP47 0 AP48 0 AP49 0 AP50 0 AP51 0 AP52 0 AP53 0 AP54 0 AP55 0 AP56 0 AP57 0 AP58 0 AP59 0 AP60 0 AP61 0 AP62 0 AP63 0 AP64 0 AP65 0 AP66 0 AP67 0 AP68 0 AP69 0 AP70 0 AP71 0 AP72 0 AP73 0 AP74 0 AP75 0 AP76 0 AP77 0 AP78 0 AP79 0 AP80 0 AP81 0 AP82 0 AP83 0 AP84 0 AP85 0 AP86 0 AP87 0 AP88 0 AP89 0 AP90 0 AP91 0 AP92 0 AP93 0 AP94 0 AP95 0 AP96 0 AP97 0 AP98 0 AP99 0 AP100 0		MUE HWSEC 000018EB UTSEC 308E7D03 UTUSEC 00000 XMITR OK RECUR OK ERRCNT 0 XMITCTR 96 RECCCTR 106 ENABLER 31 TMPSTATE 22 SPSTATE 0 STEST 0 FDM 0 CCSAP 0

CMD:IC00C00000050000FF000097

UPDATE:

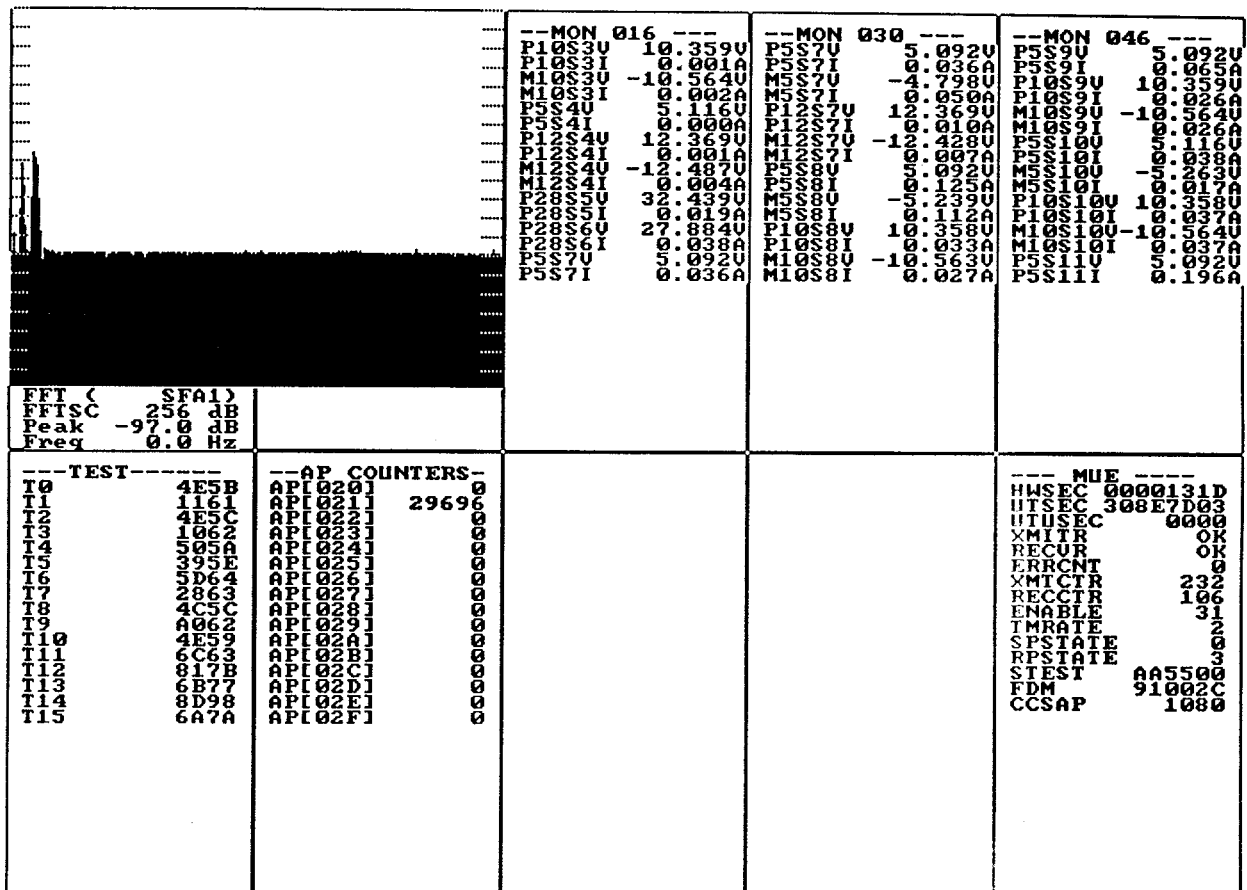
48

DATE: 03-17-94
GMT: 18:54:21

MAR 18 1994

C502
159.646 MHz

Figure 61



UPDATE: 128 DATE: 03-17-94 CRT: 18:29:35

MAR 18 1994

CSO 2

180.934

Figure 63

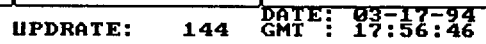
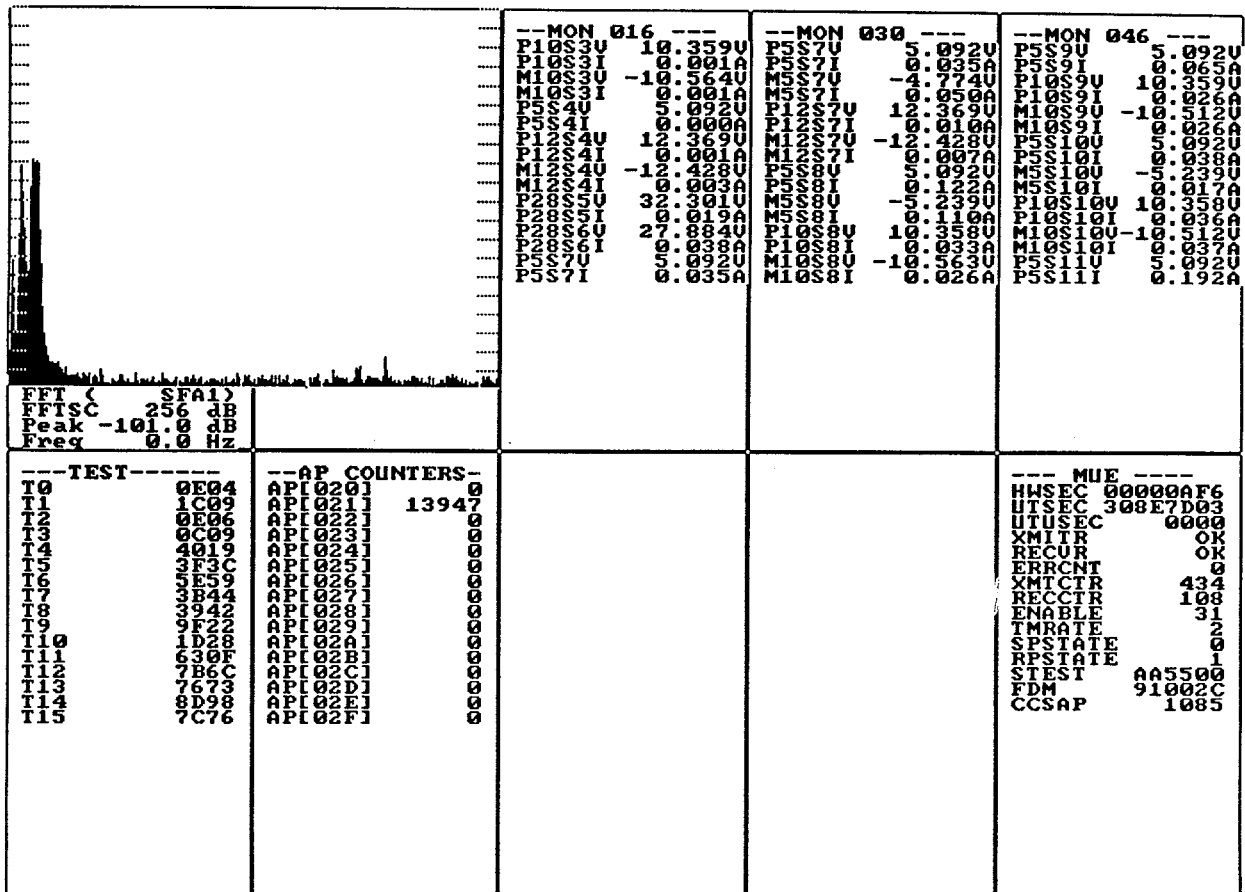


FIGURE 66



UPDATE: 128 DATE: 03-17-94 GMT: 17:54:49

C502
300 mhz

3-18-94

				-SCOPE PARAMS- ADC1 1, 32511 -OFF- 0 -OFF- -OFF- Chan. S/Dc 128.000 M/Dc 8.27 Speed PKT(01) Trig -OFF- If 0 Step 1
---TEST--- T0 F97F T1 F97F T2 FA7F T3 FA7F T4 F97F T5 F77F T6 F87F T7 F97F T8 F87F T9 FA7F T10 F87F T11 F87F T12 F87F T13 F87F T14 F87F T15 F97F	---AP COUNTERS--- API0141 0 API0151 0 API0161 0 API0171 0 API0181 2525 API0191 0 API01A1 0 API01B1 0 API01C1 0 API01D1 0 API01E1 0 API0201 0 API0211 0 API0221 0 API0231 0	---QTY OUTPUTS--- LFF1P2 0000 U1 0000 U2 0000 U3 0000 U4 0000 U5 0000 U6 0000 U7 0000 U8 0000 U9 0000 U10 0000 MAG1DC 0000 MAG2DC 0000 MAG3DC 0000 MAG1AC 0000 MAG2AC 0000	---UC COUNTERS--- UCI0001 3 UCI0011 0 UCI0021 2525 UCI0031 0 UCI0041 0 UCI0051 0 UCI0061 0 UCI0071 25742	--- MUE --- HWSEC 00000214 UTISEC 309261C1 UTUSEC 0000 XMTR OK RECUR OK ERRCNT 0 XMTCTR 96 RECCTR 106 ENABLE 31 TMRATE 2 SPSTATE 0 RPSTATE 0 STEST AA5500 FDM 91002C CCSAP 1080

CHD:IC00C00000050000FF000097

UPDRATE: 368

DATE: 03-20-94
GMT: 16:09:41

21 Mar 94
CSO 2
300 MHz

				-SCOPE PARAMS- ADC1 1, 32511 -OFF- 1, 0 -OFF- -OFF- Chan.: S/DV: 128.00 M/DV: 8.29 Speed: PKT(01) Trig: -OFF- Step: 1
---TEST--- T0 F97F T1 FA7F T2 F87F T3 FA7F T4 F77F T5 F97F T6 F77F T7 F97F T8 F77F T9 F87F T10 F87F T11 F77F T12 F97F T13 F97F T14 F97F T15 F87F	---AP COUNTERS--- AP[014] 0 AP[015] 0 AP[016] 0 AP[017] 0 AP[018] 3419 AP[019] 0 AP[01A] 0 AP[01B] 0 AP[01C] 0 AP[01D] 0 AP[01E] 0 AP[01F] 0 AP[020] 0 AP[021] 0 AP[022] 0 AP[023] 0	---QTY OUTPUTS--- LFF1P2 0000 U1 0000 U2 0000 U3 0000 U4 0000 U5 0000 U6 0000 U7 0000 U8 0000 U9 0000 U10 0000 MAC1DC 0000 MAC2DC 0000 MAC3DC 0000 MAC1AC 0000 MAC2AC 0000	---UC COUNTERS--- UC[000] 3 UC[001] 0 UC[002] 3419 UC[003] 0 UC[004] 0 UC[005] 0 UC[006] 0 UC[007] 58525	---MUE--- HWSEC 000002C0 UTSEC 309261C1 UTUSEC 0000 XMITR OK RECUR OK ERRCNT 0 XMITCTR 434 RECCTR 108 ENABLE 31 TMRATE 2 SPSTATE 0 RPSTATE 0 SIEST AA5500 FDM 91002C CCSAP 1080

CMD:1C00C00000050000FF000097

UPDATE: 416

DATE: 03-20-94
GMT: 16:12:33

C502
21 Mar 94
you m h3

Figure 70

SUMMARY

This testing has shown the level of susceptibility to EMC. In all three of the tests, particularly CS01, there was a measurable effect on the operation of the instrument. However spacecraft testing we have done indicates that the test levels greatly exceed input levels that exist on the power bus. Measurements of actual bus noise will be made during spacecraft EMC testing to provide further verification of this.

Susceptability could be reduced by adding additional filtering to the IDPU power system. This would require substantial re-work which does not appear to be justified by the test results.