



ENGINEERING CHANGE FORM			
ENGINEERING REQUEST/NOTICE Request Notice Number X 009		DRAWING/DOCUMENT TITLE DCB DMA buffer swap at 32 Hz	
DATE 8/13/2004			
SERIAL NUMBER (If Applicable)	ITEM TYPE Hardware Software X	DRAWING/DOCUMENT NUMBER thm_dcb_001_DCBSpecification	PROJECT THEMIS
REASON FOR REQUEST DCB Actel must be changed to enable a DMA buffer swap to occur at 32 Hz to accommodate 8192-samples/s rate for DFB burst channels. (Increased from baseline 16 Hz limit).			REQUESTER Vassilis Angelopoulos
			Disposition Date Effective
CHANGE DESCRIPTION (Request/Response) Revise DCB Specification Change DCB Actel Design to enable 32Hz option			1 2
DISPOSITION/REMARKS			DISPOSITION CODES 1) Change in design spec, drawing, or schematic 2) Replace 3) Rework 4) Use As Is 5) See Note/Attachment
COGNIZANT ENGINEER		Class 1 Class 2 <input type="checkbox"/> <input type="checkbox"/>	
APPROVALS (Initials/Date) Lead Engineer _____ QA Manager _____ Project Manager _____ Systems Engineer _____		CLASS CODES 1 Major Cost/Schedule 2 Minor Cost/Schedule	NEXT HIGHER ASSEMBLY IDPU _____ _____ _____